

# Network Appliance Platform

Hardware Platforms for Network Computing

# FX-3230 User Manual

Version: 1.1 Date of Release:2020-12-18

# **Icon Descriptions**

The icons are used in the manual to serve as an indication of interest topics or important messages. Below is a description of these icons:



**Note**: This mark indicates that there is a note of interest and is something that you should pay special attention to while using the product.



**Warning**: This exclamation point indicates that there is a caution or warning and it is something that could damage your property or product.

# **Online Resources**

The listed websites are links to the on-line product information and technical support.

Resources	URL	
Lanner	http://www.lannerinc.com	
Product Resource	http://www.lannerinc.com/download-center	
RMA	http://eRMA.lannerinc.com	

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### **Compliances and Certification**

### CE

This product has passed the CE test for environmental specifications. Test conditions for passing included the equipment being operated within an industrial enclosure. In order to protect the product from being damaged by ESD (Electrostatic Discharge) and EMI leakage, we strongly recommend the use of CE-compliant industrial enclosure products.

### **FCC Class A**

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. The operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

### **EMC Notice**

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. The operation of this equipment in a residential area is likely to cause harmful interference in which case users will be required to correct the interference at their own expense.

# **Safety Guidelines**

Follow these guidelines to ensure general safety:

- ▶ Keep the chassis area clear and dust-free during and after installation.
- Do not wear loose clothing or jewelry that could get caught in the chassis. Fasten your tie or scarf and roll up your sleeves.
- Wear safety glasses if you are working under any conditions that might be hazardous to your eyes.
- Do not perform any action that creates a potential hazard to people or makes the equipment unsafe.
- Disconnect all power by turning off the power and unplugging the power cord before installing or removing a chassis or working near power supplies
- ▶ Do not work alone if potentially hazardous conditions exist.
- ▶ Never assume that power is disconnected from a circuit; always check the circuit.

In order not to result in potential electric shock or fire, please avoid improper use narrated below:

- Replacing a battery with an incorrect type (e.g. in the case of certain lithium battery types), which can defeat a safety guard.
- Disposal of a battery into fire or a hot oven, or mechanically crushing or cutting of a battery, which can result in an explosion.
- Leaving a battery in an extremely high temperature surrounding environment, which can result in an explosion or the leakage of flammable liquid or gas.
- A battery subjected to extremely low air pressure that may result in an explosion or the leakage of flammable liquid or gas.

#### **Lithium Battery Caution:**

- Risk of Explosion if Battery is replaced by an incorrect type. Dispose of used batteries according to the instructions.
- Installation only by a trained electrician or only by an electrically trained person who knows all English Installation and Device Specifications which are to be applied.
- Do not carry the handle of power supplies when moving to another place.

### **Operating Safety**

- Electrical equipment generates heat. Ambient air temperature may not be adequate to cool equipment to acceptable operating temperatures without adequate circulation. Be sure that the room in which you choose to operate your system has adequate air circulation.
- ► Ensure that the chassis cover is secure. The chassis design allows cooling air to circulate effectively. An open chassis permits air leaks, which may interrupt and redirect the flow of cooling air from internal components.
- Electrostatic discharge (ESD) can damage equipment and impair electrical circuitry. ESD damage occurs when electronic components are improperly handled and can result in complete or intermittent failures. Be sure to follow ESD-prevention procedures when removing and replacing components to avoid these problems.
- Wear an ESD-preventive wrist strap, ensuring that it makes good skin contact. If no wrist strap is available, ground yourself by touching the metal part of the chassis.
- Periodically check the resistance value of the antistatic strap, which should be between 1 and 10 megohms (Mohms).
- Product shall be used with Class 1 laser device modules.

### **Mounting Installation Precaution**

#### Environment:

- Do not install and/or operate this unit in any place that flammable objects are stored or used in.
- ▶ If installed in a closed or multi-unit rack assembly, the operating ambient temperature of the rack

#### FX-3230 User Manual

environment may be greater than room ambient. Therefore, consideration should be given to installing the equipment in an environment compatible with the maximum ambient temperature (Tma) specified

by the manufacturer.

- Installation of the equipment (especially in a rack) should consider the ventilation of the system's intake (for taking chilled air) and exhaust (for emitting hot air) openings so that the amount of air flow required for safe operation of the equipment is not compromised.
- ► To avoid a hazardous load condition, be sure the mechanical loading is even when mounting.
- Consideration should be given to the connection of the equipment to the supply circuit and the effect that overloading of the circuits might have on over-current protection and supply wiring. Appropriate consideration of equipment nameplate ratings should be used when addressing this concern.
- Reliable earthing should be maintained. Particular attention should be given to supply connections other than direct connections to the branch circuit (e.g. use of power strips).

#### Installation & Operation:

- The installation of this product must be performed by trained specialists; otherwise, a non-specialist might create the risk of the system's falling to the ground or other damages.
- ► Lanner Electronics Inc. shall not be held liable for any losses resulting from insufficient strength for supporting the system or use of inappropriate installation components.

### Consignes de sécurité

Suivez ces consignes pour assurer la sécurité générale :

- Laissez la zone du châssis propre et sans poussière pendant et après l'installation.
- Ne portez pas de vêtements amples ou de bijoux qui pourraient être pris dans le châssis. Attachez votre cravate ou écharpe et remontez vos manches.
- Portez des lunettes de sécurité pour protéger vos yeux.
- N'effectuez aucune action qui pourrait créer un danger pour d'autres ou rendre l'équipement dangereux.
- Coupez complètement l'alimentation en éteignant l'alimentation et en débranchant le cordon d'alimentation avant d'installer ou de retirer un châssis ou de travailler à proximité de sources d'alimentation.
- Ne travaillez pas seul si des conditions dangereuses sont présentes.
- Ne considérez jamais que l'alimentation est coupée d'un circuit, vérifiez toujours le circuit. Cet appareil génère, utilise et émet une énergie radiofréquence et, s'il n'est pas installé et utilisé conformément aux instructions des fournisseurs de composants sans fil, il risque de provoquer des interférences dans les communications radio.
- Le produit doit être utilisé avec des modules de dispositifs laser de classe 1.

### Avertissement concernant la pile au lithium

Risque d'explosion si la pile est remplacée par une autre d'un mauvais type.
 Jetez les piles usagées conformément aux instructions.

- L'installation doit être effectuée par un électricien formé ou une personne formée à l'électricité connaissant toutes les spécifications d'installation et d'appareil du produit.
- ▶ Ne transportez pas l'unité en la tenant par le câble d'alimentation lorsque vous déplacez l'appareil.
- La machine ne peut être utilisée qu'à un lieu fixe comme en laboratoire, salle d'ordinateurs ou salle de classe.

### Sécurité de fonctionnement

L'équipement électrique génère de la chaleur. La température ambiante peut ne pas être adéquate pour refroidir l'équipement à une température de fonctionnement acceptable sans circulation adaptée. Vérifiez que votre site propose une circulation d'air adéquate.

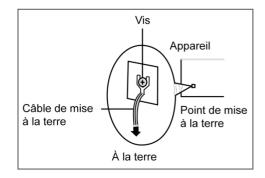
- Vérifiez que le couvercle du châssis est bien fixé. La conception du châssis permet à l'air de refroidissement de bien circuler. Un châssis ouvert laisse l'air s'échapper, ce qui peut interrompre et rediriger le flux d'air frais destiné aux composants internes.
- Les décharges électrostatiques (ESD) peuvent endommager l'équipement et gêner les circuits électriques. Des dégâts d'ESD surviennent lorsque des composants électroniques sont mal manipulés et peuvent causer des pannes totales ou intermittentes. Suivez les procédures de prévention d'ESD lors du retrait et du remplacement de composants.
- Portez un bracelet anti-ESD et veillez à ce qu'il soit bien au contact de la peau. Si aucun bracelet n'est disponible, reliez votre corps à la terre en touchant la partie métallique du châssis.
- Vérifiez régulièrement la valeur de résistance du bracelet antistatique, qui doit être comprise entre 1 et 10 mégohms (Mohms).

### Consignes de sécurité électrique

- Avant d'allumer l'appareil, reliez le câble de mise à la terre de l'équipement à la terre.
- Une bonne mise à la terre (connexion à la terre) est très importante pour protéger l'équipement contre les effets néfastes du bruit externe et réduire les risques d'électrocution en cas de foudre.
- > Pour désinstaller l'équipement, débranchez le câble de mise à la terre après avoir éteint l'appareil.
- Un câble de mise à la terre est requis et la zone reliant les sections du conducteur doit faire plus de 4 mm2 ou 10 AWG.

# Procédure de mise à la terre pour source d'alimentation CC Procédure de mise à la terre pour source d'alimentation CC

- Desserrez la vis du terminal de mise à la terre.
- Branchez le câble de mise à la terre à la terre.
- L'appareil de protection pour la source d'alimentation CC doit fournir 30 A de courant.
- Cet appareil de protection doit être branché à la source d'alimentation avant l'alimentation CC.



CAUTION: TO DISCONNECT POWER, REMOVE ALL POWER CORDS FROM UNIT. 注意:要断开电源·请将所有电源线从本机上拔下。

**WARNUNG:** Wenn Sie das Gerät zwecks Wartungsarbeiten vom Netz trennen müssen, müssen Sie beide Netzteile abnehmen.

**ATTENTION:** DÉBRANCHER LES TOUT CORDONS D'ALIMENTATION POUR DÉCONNECTER L'UNITÉ DU SECTEUR.

This equipment must be grounded. The power cord for product should be connected to a socket-outlet with earthing connection.

# **Battery Precautions**

- Lithium Battery Caution: There is danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type. Dispose batteries according to manufacturer's instructions.
- Disposal of a BATTERY into fire or a hot oven, or mechanically crushing or cutting of a BATTERY can result in an EXPLOSION.
- Leaving a BATTERY in an extremely high temperature surrounding environment can result in an EXPLOSION or the leakage of flammable liquid or gas.
- A BATTERY subjected to extremely low air pressure may result in an EXPLOSION or the leakage of flammable liquid or gas.

Suitable for installation in Information Technology Rooms in accordance with Article 645 of the National Electrical Code and NFPA 75.

Peut être installé dans des salles de matériel de traitement de l'information conformément à l'article 645 du National Electrical Code et à la NFPA 75.

The machine can only be used in a restricted access location, such as labs or computer facilities with the proper authorization.

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# **CHAPTER 1: PRODUCT OVERVIEW**

The FX-3230 is a secure, high-performance and high-capacity 2U rackmount storage gateway appliance built with Intel® Xeon® Processor Scalable Family (Skylake-SP). It comes with Intel® QuickAssist Technology and can be configured with up to 320GB of system memory, 4x 3.5" swappable HDD bays, 1x onboard mSATA slot, 4x GbE RJ45 or 4x 10G SFP+, 4x NIC module slots, 1x RJ45 & 1x mini USB consoles and 1x IPMI.

### **Package Content**

Your package contains the following items:

- 1x FX-3230 Network Appliance
- 2x US Power Cord
- ▶ 1x Console Cable (RS232)
- 1x LAN Cable (Cross-over)
- 1x LAN Cable (Straight)
- 1x Ear Kit

### **Ordering Information**

SKU No.	Main Features
FX-3230A	Intel C626 + 4x 10G SFP+ with LED + BMC,4x NIC Module Slots
FX-3230B	Intel C621 + 4x GbE RJ45 MGMT,4x NIC Module Slots
FX-3230C	Intel C627 + 4x 10G SFP+ with LED + BMC, 4x NIC Module Slots

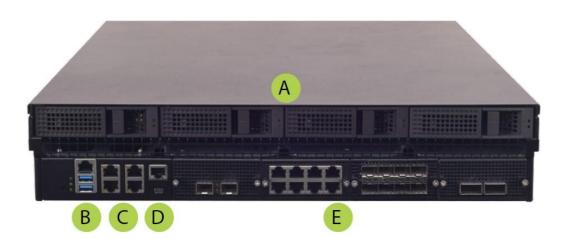


**Note**: If any component is missing or damaged, please contact your dealer immediately for assistance.

# System Specifications

Form Factor		2U 19" Rackmount
		Intel <sup>®</sup> Xeon <sup>®</sup> Processor Scalable Family
	Processor Options	(Skylake-SP)
Platform	CPU Socket	1x LGA3647
	Chipset	Intel® C621/626/627
	Security Acceleration	Intel <sup>®</sup> QuickAssist Technology (By SKU)
BIOS		AMI SPI Flash BIOS
	Technology	DDR4 2666 MHz REG DIMM
System Memory	Max. Capacity	320GB
	Socket	10 x 288pin DIMM
		4x GbE RJ45 Intel® or
	Ethernet Ports	4x 10G SFP+ Lewisburg internal MAC
Networking		(By SKU)
	Bypass	Depends on NIC module specifications
	NIC Module Slot	4
LOM	IO Interface	1x RJ45 (By SKU)
	OPMA slot	IPMI Chip onboard (SKU A &C)
	Reset Button	
	LED	Power/Status/Storage
	Power Button	1x ATX Power Switch
I/O Interface	Console	1x RJ45
	USB	1x mini USB Console (By SKU) 2x USB 3.0
	LCM Module	
		N/A (Optional)
	Display Power input	1x VGA (Internal Pin Header) AC power inlet on PSU
	HDD/SSD Support	4x 3.5" swappable
Storage	Onboard Slots	1x mSATA
	PCle	1x PCI-E*8 FH/HL (Optional)
Expansion	mini-PCle	N/A
	SIM card Slot	N/A
	Watchdog	YES
Miscellaneous	Internal RTC with Li Battery	YES
	ТРМ	YES (Optional)
	Processor	Passive CPU heat sink
Cooling		4x individual hot-swappable cooling
	System	fans with smart fan
	Tomporaturo	0~40°C Operating
Environmental Parameters	Temperature	-20~70°C Non-Operating
Livionnentari arameters	Humidity (RH)	5~90% Operating
		5~ 95% Non-Operating
System Dimensions	(WxDxH)	438 x 600 x 88 mm
	Weight	13kg
Package Dimensions	(WxDxH)	935x588x258mm
	Weight	20kg
Power	Type/Watts	550W 1+1 Redundant PSUs
Power		550W 1+1 Redundant PSUs AC 100~240V @47~63 Hz

# **Front Panel**



- A 4x 3.5" Swappable HDD
- B Console (RJ45) & 2x USB 3.0
- C 4x GbE RJ45 or 10G SFP+
- D Console (Mini USB) & IPMI
- E 4x NIC Modules

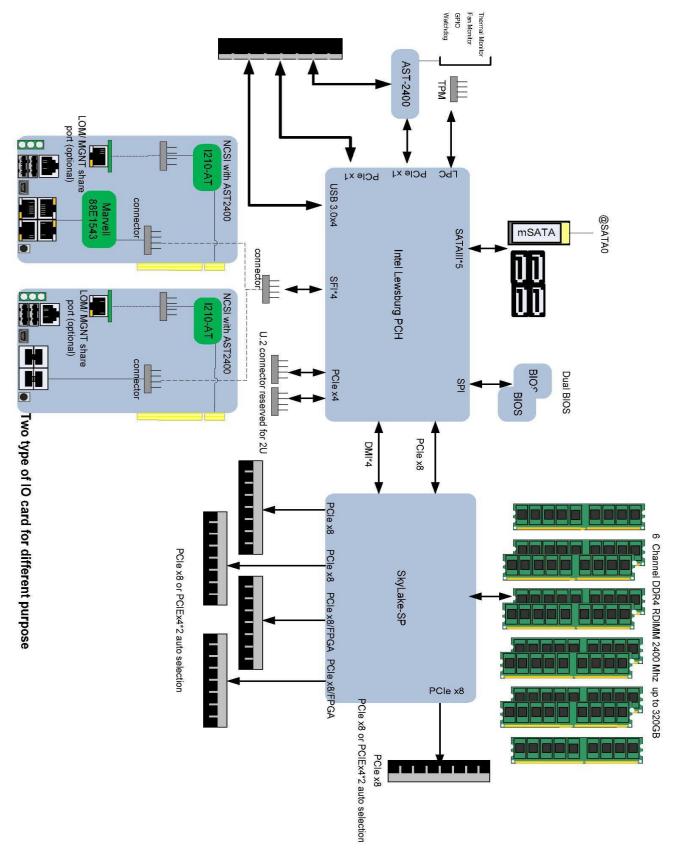
# **Rear Panel**



# **CHAPTER 2: MOTHERBOARD INFORMATION**

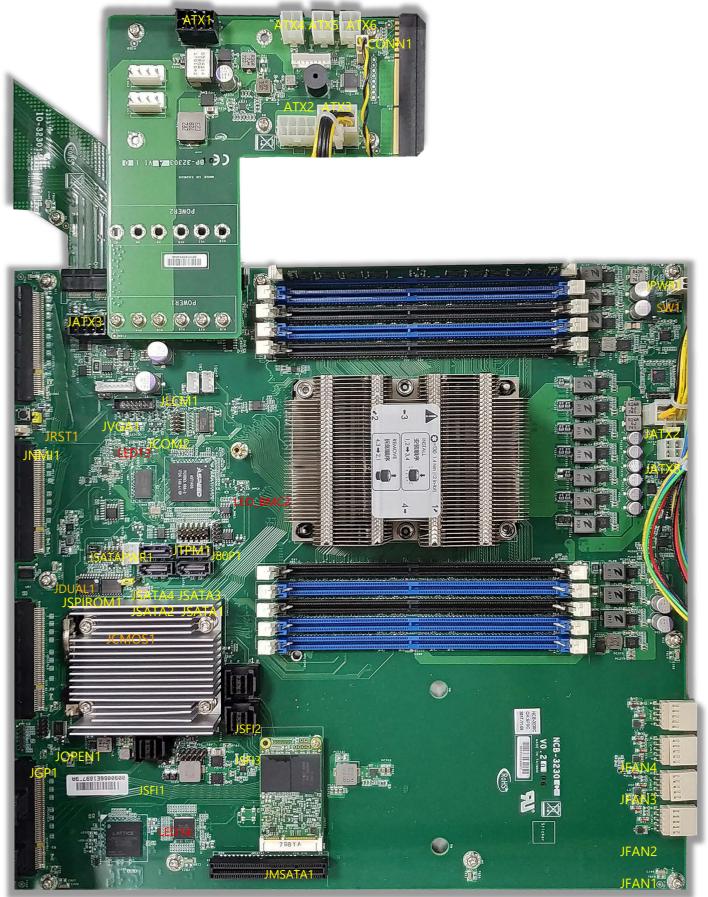
# **Block Diagram**

The block diagram indicates how data flows among components on the motherboard. Please refer to the following figure for your motherboard's layout design.



# **Motherboard Layout**

The motherboard layout shows the connectors and jumpers on the board. Refer to the following picture as a reference of the pin assignments and the internal connectors.



# **Internal Jumper & Connectors**

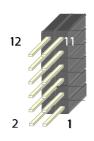
JRST1:

Setting	Description	Setting	Description
1.2 • • • • • • • • • • • • • • • • • • •	HW reset	2.3	SW reset



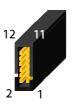
# FX-3230 User Manual JTPM1: Supports TPM 1.2 & 2.0

Pin	Description	Pin	Description
1	IRQ_SERIAL	2	LPC_LFRAME#
3	LPC_LAD0	4	CLK_24M_LPC
5	LPC_LAD1	6	+P3V3_AUX
7	LPC_LAD2	8	
9	LPC_LAD3	10	+P3V3
11	TPM_RST#	12	GND



### JVGA1

Pin	Description	Pin	Description
1	DAC_RO	2	GND
3	DAC_GO	4	GND
5	DAC_BO	6	GND
7	HSYNC_O	8	
9	VSYNC_O	10	GND
11	DDC_DATA	12	DDC_CLK



### ATX6: 8-Pin Power Connector

Pin	Description	Pin	Description
1	GND	2	+P5V
3	GND	4	+P5V_SB
5	GND	6	+P12V_STBY_PSU
7	GND	8	+P12V_STBY_PSU



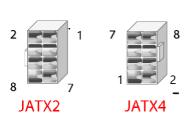
### JATX8: 4 pin Power Connector (12V standby)

Pin	Description	Pin	Description	
1	GND	2	+P12V_1_STBY	
3	GND	4	+P12V_1_STBY	

3	4
1	2

### JATX2 & JATX4: 8 pin Power Connector

Pin	Description	Pin	Description
1	GND	2	+12V
3	GND	4	+12V
5	GND	6	+12V
7	GND	8	+12V



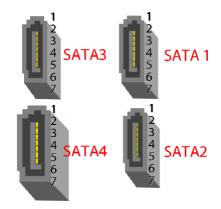
### JATX9: 4 pin Power Connector (12V standby)

Pin	Description	Pin	Description
1	GND	2	+P12V_2_STBY
3	GND	4	+P12V_2_STBY



### JSATA1~JSATA4: SATA Port

Pin	Description	Pin	Description
1	GND	2	TX_P
3	TX_N	4	GND
5	RX_N	6	RX_P
7	GND		



### JFAN1~4: FAN Connector

Pin	Description	Pin	Description
1	GND	2	12V
3	RPM Sense	4	RPM Sense
5	WM Status		

#### JNMI1:

Pin	Description	Pin	Description
1	GND	2	NMIBTN#



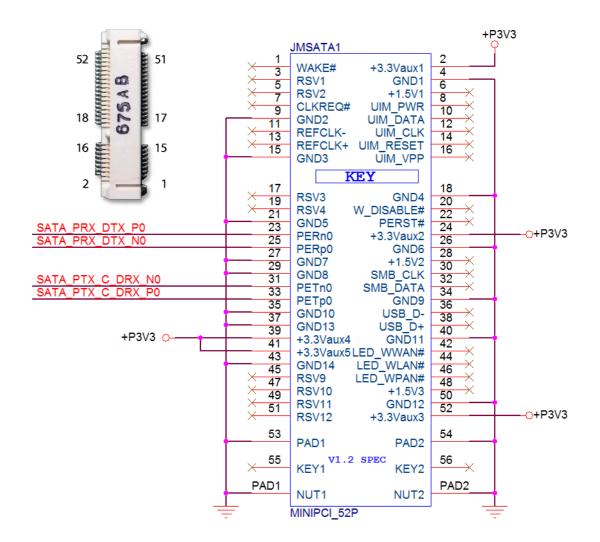
JFAN4

JFAN2

JFAN1

54321 JFAN3

### MSATA1:



Pin	Description	Pin	Description	Pin	Description	Pin	Description
1	WAKE#	2	+3.3Vaux1	3	RSV1	4	GND1
5	RSV2	6	+1.5V1	7	CLKREQ	8	UIM_PWR
9	GND2	10	UIM_DATA	11	REFCLK-	12	UIM_CLK
13	REFCLK+	14	UIM_RESET	15	GND3	16	UIM_VPP
17	RSV3	18	GND4	19	RSV4	20	W_DISABLE#
21	GND5	22	PERST#	23	PERn0	24	+3.3Vaux2
25	PERp0	26	GND6	27	GND7	28	+1.5V2
29	GND8	30	SMB_CLK	31	PETn0	32	SMB_DATA

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33	PETp0	34	GND9	35	GND10	36	USB_D-
37	GND13	38	USB_D+	39	+3.3Vaux4	40	GND11
41	+3.3Vaux5	42	LED_WWAN#	43	GND14	44	LED_WLAN#
45	RSV9	46	LED_WPAN#	47	RSV10	48	+1.5V3
49	RSV11	50	GND12	51	RSV12	52	+3.3Vaux3

### JCOM2: COM PORT

Pin	Description	Pin	Description	
1	BMC_COM2_DCD#	2	BMC_COM2_DSR#	
3	BMC_COM2_RX	4	BMC_COM2_RTS	11111
5	BMC_COM2_TX	6	BMC_COM2_CTS#	9
7	BMC_COM2_DTR	8	BMC_COM2_RI#	8 4 4 4 2
9	COM2_GND2			

#### JLCM1: UART2

Pin	Description
1	BMC_COM2_DCD#
2	BMC_COM2_RX
3	BMC_COM2_TX
3	BMC_COM2_DTR



**Note**: The on-board BMC supports two UARTs: UART1 for console and UART2 for LCM or COM2. If UART2 is to be used for connecting LCM, the on-board COM2 pin header or a reserved COM port opening on rear panel will be of no necessity.

### PMB1: PMBUS

Pin	Description	Pin	Description
1	PSU_TTL1	2	PSU_TTL2
3	ATX_PSON#	4	GND
5	ATXPWGD	6	PMBUS_CLK
7	PMBUS_DAT	8	PMBUS_ALERT#



# FX-3230 User Manual **JOPEN1**: Case open



Pin	Description	Pin	Description
1	GND	2	FM_INTRUDER#

### JCMOS1: Clear CMOS

Pin	Description	Pin	Description
1	VRTC	2	PCH_RTCRST#
3	GND		



Normal (Default) O

3

2



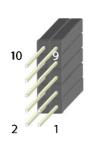
**Clear CMOS** 

### JSPIROM1: Flash BIOS

Pin	Description	Pin	Description
1	SPI_HD1#	2	SPI_CS1#_DUAL
3	SPI_CS0#_DUAL	4	+P3V3_SPI_ME
5	SPI_MISO	6	SPI_PCH_IO3
7		8	SPI_CLK
9	GND	10	SPI_MOSI

JGP1: EXT GPIO header

Pin	Description	Pin	Description
1	GPO_B_1	2	GPI_B_1
3	GPO_B_2	4	GPI_B_2
5	GPO_B_3	6	GPI_B_3
7	GPO_B_4	8	GPI_B_4
9	GND	10	GND

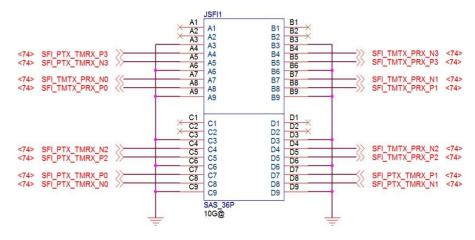


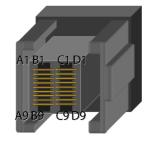
1

10

2

JSFI1: SFP+ (for fiber cable)





### JPWRON1: Power on

Pin	Description	Pin	Description
1	GND	2	ATX_PSON#



### JIOB1: IO board

	P4	JIOB1		7 \ 1	
<80> CLK_BMC_125M <<-	B1	12V_A	PRSNT1*	A1	
100. 0E/(_DINO_12011 ((	<u>B2</u>			A2	
	<b>B</b> 3	12V_B	12V_C	A3	
	A B4	12V_D	12V_E	A4	
_	B5	GND1	GND2	A5	
<72> USB20_P6 < >	B6	SMCLK	JTAG2	A6	
<72> USB20_N6 <		SMDAT	JTAG3	47	
	B7	GND3		A7 A8 ×	// <u>-</u>
<72> USB20 P5	B8		JTAG4		
- ~	B9	3.3V_A	JTAG5	A9	
<72> USB20_N5 <	B10	JTAG1	3.3V_B	A10	
	B11	3.3VAUX	3.3V_C	A11	PCIE_WAKE# <51,52,53,54,55,56,74,89>
	<u> </u>	WAKE#	PERST#		
	B40		KEY	442	··· -
<72> USB30_TX3P <<<	B12	RSVD A	GND4	A12	
<72> USB30_TX3P <72> USB30_TX3N	B13			A13	BMC UART1 CTS# <58>
\$728_03B30_1X3N	B14	GND5	REFCLK_+_H	A14	
	B15	HSOP0_H	REFCLKL	A15	
<72> USB30_TX4P <<<	B16	HSON0_L	GND6	A16	
<72> USB30_TX4N <<<	B17	GND7	HSIP0_H	A17	
	B18	PRSNT2#	HSIN0_L	A18	
	010	GND8	GND9	AID	BMC_UART1_TX <58>
		01100	0,100		
	B19			A19	
<72> USB30_RX3P >>	B20	HSOP1_H	RSVD_B	A20	SMB_PCH10G_SDA3 <75>
<72> USB30_RX3N >>	B21	HSON1_L	GND10	A21	
	B22	GND11	HSIP1_H	A22	SMB_PCH10G_SDA2 <75>
<72> USB30_RX4P >>>	B23	GND12	HSIN1_L	A23	
<72> USB30_RX4N >>>		HSOP2 H	GND13	A24	
- //	B24	HSON2 L	GND14		SMB_PCH10G_SCL1 <75>
<73> PCIE_PTX_C_LANRX_P0 >>>	B25			A25	
	B26	GND15	HSIP2_H	A26	
<73> PCIE_PTX_C_LANRX_N0 >>	B27	GND16	HSIN2_L	A27	<pre> SMB_PCH10G_SCL0 &lt;75&gt;</pre>
2795 BOIE DOV O LANTY DO	B28	HSOP3_H	GND17	A28	//
<73> PCIE_PRX_C_LANTX_P0 <<	B29	HSON3_L	GND18	A29	
<73> PCIE_PRX_C_LANTX_N0 <	B29 B30	GND19	HSIP3_H	A30	
		RSVD C	HSIN3_L		
	B31			A31	
	B32	PRSNT2#	GND20	A32	(( PM_E2_EVG5_MOD_AB30 \$158
		GND21	RSVD_D		
	B33			A33	
<73> PCIE_PTX_C_LANRX_P1 >>	B34	HSOP4 H	RSVD_E	A34	
<73> PCIE_PTX_C_LANRX_N1 >>	B35	HSON4 L	GND22	A35	
		GND23	HSIP4 H		
<73> PCIE_PRX_C_LANTX_P1 /</td <th>B36</th> <td>GND24</td> <td>HSIN4_L</td> <td>A36</td> <td></td>	B36	GND24	HSIN4_L	A36	
<73> PCIE_PRX_C_LANTX_N1 2	B37			A37	(( im_iz_cros_mos_isse iss
	B38	HSOP5_H	GND25	A38	// 155 0551 05555
	B39	HSON5_L	GND26	A39	
	B40	GND27	HSIP5_H	A40	
<30> CLK_PCIE_LAN1_P >>	B41	GND28	HSIN5 L	A41	
<30> CLK_PCIE_LAN1_N >>	B42	HSOP6 H	GND29	A42	————————————————————————————————————
		HSON6_L	GND30		
<30> CLK_PCIE_LAN2_P >>	B43			A43	LED_GBE0_SPEED <75>
	B44	GND31	HSIP6_H	A44	
<30> CLK_PCIE_LAN2_N >>	B45	GND32	HSIN6_L	A45	LED_GBE0_ACT <75>
	B46	HSOP7_H	GND33	A46	
	B47	HSON7_L	GND34	A47	————————————————————————————————————
	B48	GND35	HSIP7_H	A48	
	B49	PRSNT2#	HSIN7 L		
	049	GND36	GND37	A49	
	l l	011030	01001		// ····_
	B50			A50	
<80> BMC_RGMII_RXCTL <	B51	HSOP8_H	RSVD_F	A51	USB20_N1 <72>
<80> BMC_RGMII_RXD0 55	B52	HSON8_L	GND38	A52	USB20_P1 <72>
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<80> BMC_RGMII_RXD2 <	B54	GND40	HSIN8_L	A54	
<80> BMC_RGMII_RXD3 🛛 🔆	855	HSOP9_H	GND41	A55	
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	B57	GND43	HSIP9_H	A57	
<80> BMC_RGMII_TXCTL <		GND44	HSIN9_L		
<80> BMC_RGMII_TXD0 🛛 兴	B58 B59	HSOP10 H	GND45	A58	
<80> BMC_RGMII_TXD1 🔾	B60	HSON10_L	GND46		
<80> BMC_RGMII_TXD2 🔾		GND47	HSIP10_H	A60	+DEV CD
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	B62			A62	Ý
<80> BMC_RGMII_TXCLK <<	B63	HSOP11_H	GND49	7.00	
2805 BMC MA DOMINING N	B64	HSON11_L	GND50	A64	
<80> BMC_M1_RGMIILINK >>	B65	GND51	HSIP11_H	A65	
<pre>&lt;80&gt; BMC_RGMII_MDIO &lt; &gt;</pre>	B66	GND52	HSIN11_L	A66	
	B67	HSOP12_H	GND53	A67	I
<80> BMC_RGMII_MDC	007	HSON12_L	GND54	A68	+P3V3_AUX
	B68		HSIP12_H	A69	
<80> BMC_RGMII_MDC >>> <81> BMC_MAC2_RST# >>>		GND55	1000 12_11		
<80> BMC_RGMII_MDC	B68 B69	GND55 GND56	HSIN12_L		
<80> BMC_RGMII_MDC >>> <81> BMC_MAC2_RST# >>>	868 869 870	GND56	HSIN12_L	A70 ×	
<pre>&lt;60&gt; BMC_RGMII_MDC &lt;61&gt; BMC_MAC2_RST# &lt;60,61&gt; BMC_SRST# &gt;&gt;</pre>	868 869 870 871	GND56 HSOP13_H	HSIN12_L GND57	A70 × A71	
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80,81&gt; BMC_SRST# &lt;88&gt; FP_RESET#</pre>	868 869 870 871 872	GND56 HSOP13_H HSON13_L	HSIN12_L GND57 GND58	A70 × A71 A72	
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80,81&gt; BMC_SRST# &lt;88&gt; FP_RESET# &lt;72&gt; USB20_N2&lt;</pre>	868 869 870 871 872 873	GND56 HSOP13_H HSON13_L GND59	HSIN12_L GND57 GND58 HSIP13_H	A70 × A71 A72 A73	
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80,81&gt; BMC_SRST# &lt;88&gt; FP_RESET#</pre>	B68 B69 B70 B71 B72 B73 B73 B74	GND56 HSOP13_H HSON13_L GND59 GND60	HSIN12_L GND57 GND58 HSIP13_H HSIN13_L	A70 × A71 A72 A73 A74	
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80,81&gt; BMC_SRST# &lt;88&gt; FP_RESET# &lt;72&gt; USB20_N2 &lt;72&gt; USB20_P2 </pre>	B68 B69 B71 B72 B73 B74 B74 B75	GND56 HSOP13_H HSON13_L GND59 GND60 HSOP14_H	HSIN12_L GND57 GND58 HSIP13_H HSIN13_L GND61	A70 × A71 A72 A73 A74 A75	+P3V3
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80.81&gt; BMC_SRST# &lt;88&gt; FP_RESET# &lt;72&gt; USB20_N2 &lt;72&gt; USB20_P2 &lt;72&gt; USB30_RX2P </pre>	B68 B69 B70 B71 B72 B73 B73 B74	GND56 HSOP13_H HSON13_L GND59 GND60 HSOP14_H HSON14_L	HSIN12_L GND57 GND58 HSIP13_H HSIN13_L GND61 GND62	A70 × A71 A72 A73 A74 A75 A76 ×	+P3V3
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80,61&gt; BMC_SRST# &lt;88&gt; FP_RESET# &lt;72&gt; USB20_N2 &lt;72&gt; USB20_P2 </pre>	B68 B69 B71 B72 B73 B74 B74 B75	GND56 HSOP13_H HSON13_L GND59 GND60 HSOP14_H HSON14_L GND63	HSIN12_L GND57 GND58 HSIP13_H HSIN13_L GND61 HSIP14_H	A70 × A71 A72 A73 A74 A75	
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80,61&gt; BMC_SRST# &lt;88&gt; FP_RESET# &lt;72&gt; USB20_N2 &lt;72&gt; USB20_P2 &lt;72&gt; USB30_RX2P &lt;72&gt; USB30_RX2P &lt;72&gt; USB30_RX2N </pre>	B68 B69 B70 B71 B72 B73 B73 B74 B75 B75 B76	GND56 HSOP13_H HSON13_L GND59 GND60 HSOP14_H HSON14_L GND63 GND64	HSIN12_L GND57 GND58 HSIP13_H HSIN13_L GND62 HSIP14_H HSIN14_L	A70 × A71 A72 A73 A74 A75 A76 ×	
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80,61&gt; BMC_SRST# &lt;88&gt; FP_RESET# &lt;72&gt; USB20_N2 &lt;72&gt; USB20_P2 &lt;72&gt; USB30_RX2P &lt;72&gt; USB30_RX2P &lt;72&gt; USB30_RX2N &lt;72&gt; USB30_RX2P &lt;72&gt; USB30_RX2P &lt;72&gt; USB30_RX2P &lt;72&gt; USB30_RX2P </pre>	B68 B69 B70 B71 B72 B73 B74 B75 B76 B77 B77 B77 B77 B78	GND56 HSOP13_H HSON13_L GND59 GND60 HSOP14_H HSON14_L GND63	HSIN12_L GND57 GND58 HSIP13_H HSIN13_L GND61 HSIP14_H	A70 × A71 A72 A73 A74 A75 A76 A77 A78	
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80,61&gt; BMC_SRST# &lt;88&gt; FP_RESET# &lt;72&gt; USB20_N2 &lt;72&gt; USB20_P2 &lt;72&gt; USB30_RX2P &lt;72&gt; USB30_RX2P &lt;72&gt; USB30_RX2N </pre>	B68 B69 B70 B71 B72 B73 B74 B75 B76 B76 B77 B78 B79 B79	GND56 HSOP13_H HSON13_L GND59 GND60 HSOP14_H HSON14_L GND63 GND64	HSIN12_L GND57 GND58 HSIP13_H HSIN13_L GND62 HSIP14_H HSIN14_L	A70 × A71 A72 A73 A73 A74 A75 A76 A77 A77 A78 A78 A79	
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80.81&gt; BMC_SRST# &lt;88&gt; FP_RESET# &lt;72&gt; USB20_N2 &lt;72&gt; USB20_P2 &lt;72&gt; USB30_RX2P </pre>	B68 B69 B70 B71 B72 B73 B74 B75 B76 B75 B76 B77 B78 B79 B79 B79 B79 B79 B79 B79 B79 B79 B79	GND56 HSOP13_H HSON13_L GND59 GND60 HSOP14_H HSON14_L GND63 GND64 HSOP15_H	HSIN12_L GND57 GND58 HSIP13_H HSIN13_L GND61 HSIP14_H HSIN14_L GND65 GND66	A70 × A71 A72 A73 A74 A75 A76 A77 A78 A78 A79 A80	
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80.81&gt; BMC_SRST# &lt;88&gt; FP_RESET# &lt;72&gt; USB20_N2 &lt;72&gt; USB20_P2 &lt;72&gt; USB30_RX2P </pre>	B68 B69 B70 B71 B72 B73 B74 B75 B76 B76 B77 B78 B79 B80 B80 B80	GND56 HSOP13_H HSON13_L GND59 GND50 HSOP14_H HSON14_L GND63 GND64 HSOP15_H HSON15_L GND67	HSIN12_L GND57 GND58 HSIP13_H HSIN13_L GND62 HSIP14_H HSIN14_L GND65 GND66 HSIP15_H	A70 × A71 A72 A73 A74 A75 A76 A77 A78 A79 A80 A81	
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80.81&gt; BMC_SRST# &lt;88&gt; FP_RESET# &lt;72&gt; USB20_N2 &lt;72&gt; USB20_P2 &lt;72&gt; USB30_RX2P </pre>	B68 B69 B70 B71 B72 B73 B74 B75 B76 B75 B76 B77 B78 B79 B79 B79 B79 B79 B79 B79 B79 B79 B79	GND56 HSOP13_H HSON13_L GND59 GND60 HSOP14_H HSON14_L GND63 GND64 HSOP15_H HSON15_L GND67 PRSNT2#	HSIN12_L GND57 GND58 HSIP13_H HSIN13_L GND62 HSIP14_H HSIN14_L GND65 GND66 HSIP15_H HSIN15_L	A70 × A71 A72 A73 A74 A75 A76 A77 A78 A78 A79 A80	
<pre>&lt;80&gt; BMC_RGMII_MDC &lt;81&gt; BMC_MAC2_RST# &lt;80.81&gt; BMC_SRST# &lt;88&gt; FP_RESET# &lt;72&gt; USB20_N2 &lt;72&gt; USB20_P2 &lt;72&gt; USB30_RX2P </pre>	B68 B69 B70 B71 B72 B73 B74 B75 B76 B76 B77 B78 B79 B80 B80 B80	GND56 HSOP13_H HSON13_L GND59 GND50 HSOP14_H HSON14_L GND63 GND64 HSOP15_H HSON15_L GND67	HSIN12_L GND57 GND58 HSIP13_H HSIN13_L GND62 HSIP14_H HSIN14_L GND65 GND66 HSIP15_H	A70 × A71 A72 A73 A74 A75 A76 A77 A78 A79 A80 A81	

# **CHAPTER 3: HARDWARE SETUP**

To reduce the risk of personal injury, electric shock, or damage to the system, please <u>remove all power</u> <u>connections to completely shut down the device</u>. Also, please <u>wear ESD protection gloves when conducting</u> <u>the steps</u> in this chapter.

# **Opening the Chassis**

- Loosen the 2 thumb screws from the rear panel of FX-3230.
- **2.** Gently pull the cover backward a bit.
- **3.** Lift the cover up to remove it.





**4.** Remove the cover that protects the CPUs and the fans.



# **Installing the CPU**

Please note that the system delivered to you is already installed with the processor and that this processor, LGA3647, comes with rather sophisticated design; therefore, the assembly of which must be handled with exclusive tools and extreme care by professionals. It is strongly recommended that you not make any adjustments to, remove or even re-install the processor on your own. If handling the processor on your own is inevitable, please read through the instructions in this section and refer to the <u>official tutorial</u> released by Intel ® to make sure you have acquired the necessary knowledge and comply with the requirements.

Installing the processor onto motherboard involves two stages:

- 1. Mount the processor onto the heat sink to make a PHM (Processor + Heat Sink Module)
- 2. Install the PHM onto the motherboard.

Tool	Description	
Torque screwdriver (Star T30)	Set to <u>1.36 N.m</u> . or <u>12 in-lbf</u> for tightening the nuts which fasten the PHM on the bolster plate.	E Start
ESD Protection (ESD gloves, ESD-safe work surface, etc.)	During the entire assembly process, at least wear a pair of ESD gloves to avoid damaging or contaminating the electronic parts while enhancing your own safety.	

### **Tools Required**



**Note**: The images of tools shown in this document are merely for reference; the actual tools you use might differ.

### Parts Explanation:

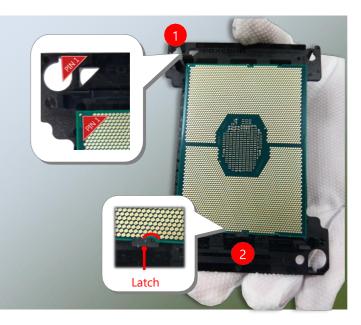
Item	Description		
Processor	Please avoid touching the gold fingers or package lands of the processor even if you are wearing ESD gloves.		

Heat Sink	If a TIM (Thermal Interface Material) protective film is already attached to the base of the heat sink, remove it before you mount the processor on it. When holding it, please grip it along the axis of its fins with your thumb and your index finger.	Axis
Processor Carrier	This is packed along with the processor. Before performing any assembly involving this part, please locate PIN1 on one of the corners, an important indicator used to align this carrier with the processor and the bolster plate correctly.	
Dust Cover	This cover is used to protect the package land surface of the processor from contamination. To remove it from the processor, grasp the holding features with your thumb and your index finger while pulling the cover off vertically.	
Bolster Plate	A robust bolster plate is used to assist in PHM alignment for installation, while effectively helping eliminate PCB bowing during compression. Please locate the Cutout on one of the four corners before starting PHM installation.	Cutout

### Mounting the CPU onto the Heat Sink

**1.** Align the PIN1 indicator on the processor with that on the carrier.

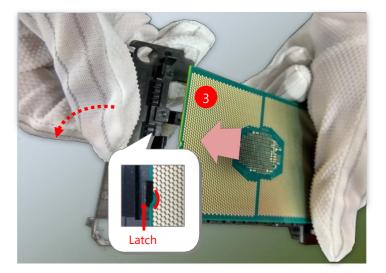
 Gently insert one side of the processor into the carrier and make sure the alignment feature is aligned with the latch of the carrier.



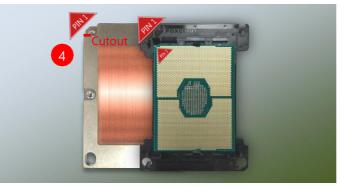


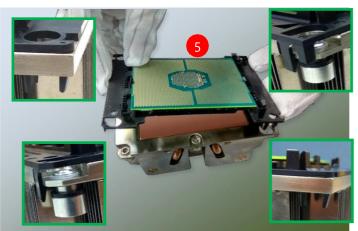
**Note**: During assembly, it is essential to have (1)PIN1 on the processor aligned with that on the carrier, and (2) the alignment features on the top and the bottom of the CPU aligned with the corresponding carrier latches.

**3.** For the other end of the carrier, align the alignment feature of the processor with the carrier latch, and then gently bend over the carrier end to have the latch clamp on the processor.



- **4.** Align PIN1 of the processor with the corner cutout of the heat sink (if there are two corner cutouts on one heat sink, either will do).
- 5. With a little pressure, push the four corners of the carrier down to engage their latching features with the corresponding corners of the heat sink. You might hear a clicking sound when the latch clicks into place.
- 6. Go through the four corners to check if the latches are engaged. If correctly latched, the corners of the carrier should be tightly attached to the heat sink, and no gap in-between is observed.







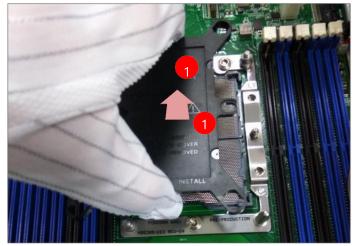
### Installing the PHM onto the Motherboard

**1.** Remove the dust cover from the socket contacts of the motherboard.

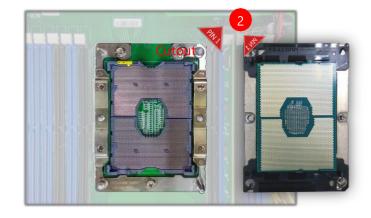


**Note**: Inspect the surface of the socket under sufficient light to ensure there is no

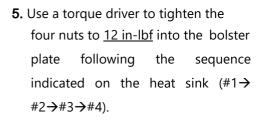
contamination or damage prior to the PHM installation.

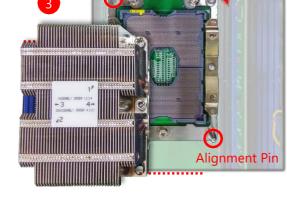


 Flip the PHM over to align PIN1 of the carrier with the Cutout of the bolster plate.



- **3.** Flip the PHM over, with the package land of the processor facing the socket, carefully hold the PHM while lowering it vertically to engage it to the alignment pins of the bolster plate.
- **4.** Make sure the PHM is sitting horizontally on the bolster plate.





Alignment Pin

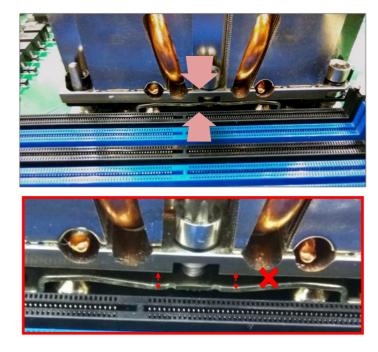






**Note**: When fastening #3 and #4 nuts, the gap between the metal spring leaf of the bolster

plate and the PHM will gradually diminish as you drive the nuts.



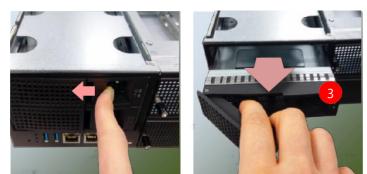
# Installing the Disk Drive(s)

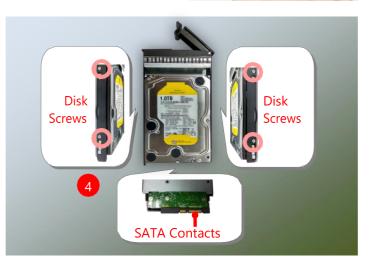
FX-3230 is built with two 3.5" HDD/SSD slot (HDD preferred) drive bay. The following will discuss disk drive installation procedures based on their HDD/SSD designs.

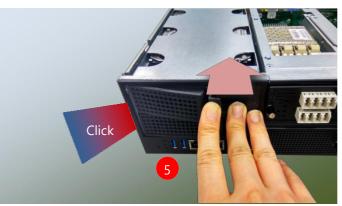
- **1.** Power off the system.
- **2.** Locate the 3.5" disk bay on the front panel.
- 3. To remove the tray, put your finger on the tab and push it to the left to slide it open, hold the tab lever and pull out the tray.
- 4. To mount the disk onto the empty tray, secure the hard disk on the tray with the provided disk screws. Make sure the disk connector faces towards the SATA contacts inside the system.

 To install the mounted disk tray, push the tray into position in the chassis. Press the hinge tab until it clicks into place.









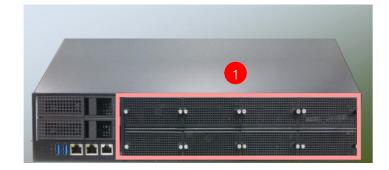
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# Installing the Disk Drive(s)

FX-3230 comes with 8 NIC Ethernet module slots for network bandwidth expansion. Please follow the steps for installation.

1. On the front panel, select a NIC

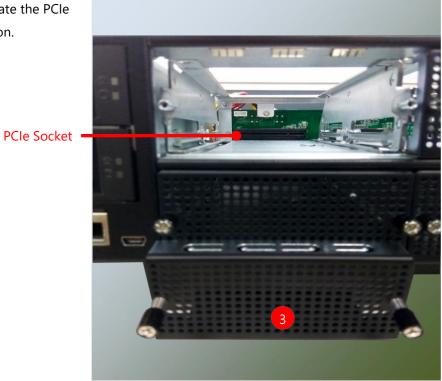
Ethernet module slot.



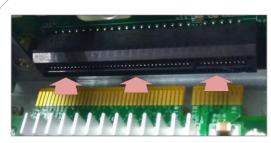
 Rotate clockwise and loosen the two lock-screws.



**3.** Remove the door and locate the PCIe socket for module insertion.



**4.** Insert your NIC Ethernet module. (The module shown in the image below is for reference only).



Align the golden fingers to the PCIe socket on the motherboard carefully while inserting this module.

**5.** Once the module is firmly seated, rotate counter-clockwise and tighten the two lock-screws.

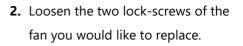


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# **Replacing the Cooling Fans**

Cooling fans may wear down eventually. Please refer to the steps below for replacing cooling fans. When using a new cooling fan, simply reverse the steps to install the fan back onto the enclosure and the system.

**1.** Locate the cooling fans at the rear panel.



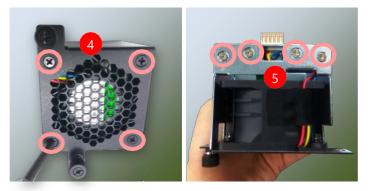


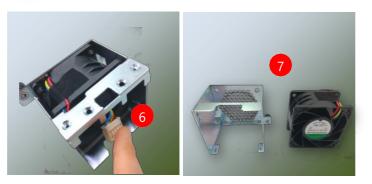


**3.** Hold onto the two lock-screws and pull it out.



- **4.** Remove the 4 screws that secure the fan.
- **5.** Remove the 4 screws that enclose the fan.
- **6.** Take the fan connector out of the enclosure.
- 7. Take the worn cooling fan out.





## **Replacing the Power Supply Units**

Power supply units may wear down eventually. Please be noted that FX-3230 series supports 550W/800W depending on the ordering preferences. Please prepare the power supply units matching this capacity.

 On the rear panel, locate the power supply units and disconnect the power cords.

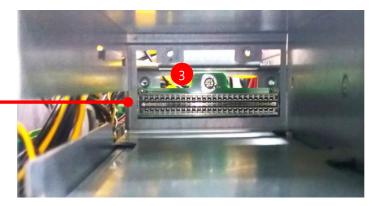


2. Pull the system out.



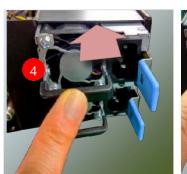
**3.** Locate the internal connector of the power supply unit.

Power supply connector,



.

**4.** Insert a new power supply unit. Push the unit in until it clicks into place.

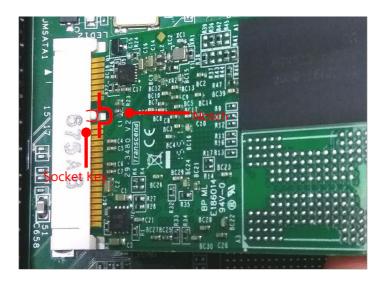




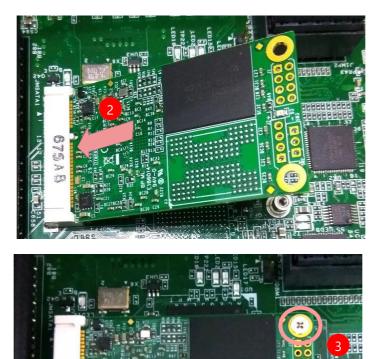
## **Installing mSATA**

The motherboard provides one mSATA slot. Follow the procedures below for installing an mSATA card.

 Locate the mSATA socket. Align the notch of the DIMM module with the socket key in the slot.



 Insert the module at 30 degrees into the socket until it is fully seated in the connector.

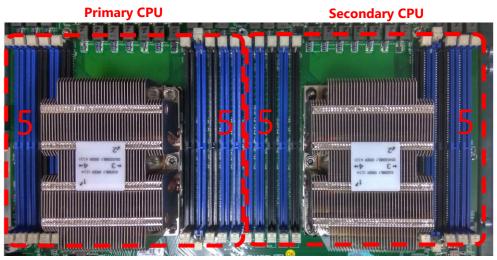


**3.** Push down on the module and secure it with screws that come with it.

675 A B

## **Installing the System Memory**

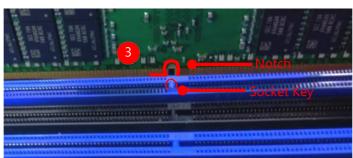
The motherboard supports DDR4 registered DIMM memory for heavy-duty operations. Please follow the steps below to install the DIMM memory modules. The primary CPU and the secondary CPU both have 10 DIMM sockets (5 on its both sides), If you do not plan to fill up all the sockets with 20 memory modules, always start with the blue ones for optimal performance.



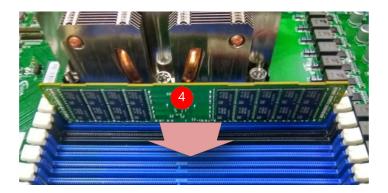
- **1.** Power off the system.
- 2. Pull open the DIMM slot latches.



**3.** Align the notch of the DIMM module with the socket key in the slot.



 Insert the module into the slot until it is firmly seated. The motherboard of is designed with <u>20</u> DDR DIMM sockets.

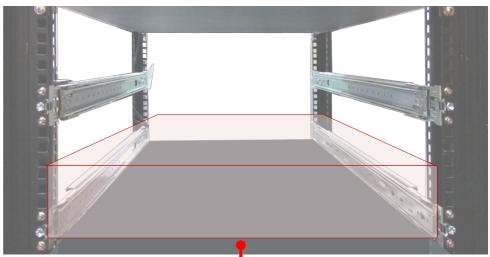


## **Mounting the System**

There are two methods for installing this system in a rack:

#### ► With Mounting Ear Brackets

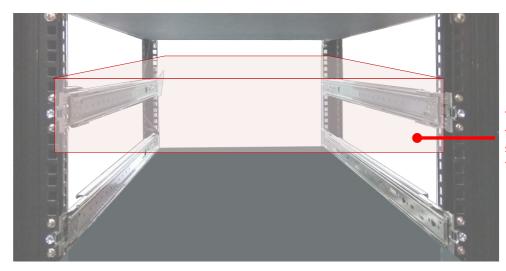
This method is quick and easy by fixing this system to the front posts of the rack, but it also makes servicing the system more difficult. Please note that the use of these brackets must go with a rack shelf or slide rails to prevent the chassis from falling over, for the <u>bracket assembly alone cannot provide sufficient support to the chassis</u>.



The system shall be installed on the rack along with a shelf or slide rails, for the "Mounting Ears" are meant to secure the system, not to support it.

#### With Slide Rail Kit + Short Mounting Ear Brackets

This method is rather complicated, but the slidable rails allow you to access the system easily, while securing it in the rack solidly.



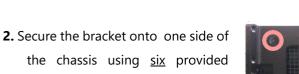
The Slide Rail Kit can secure the system while providing sufficient weight support for the device.

## Installing the System Using Mounting Ear Bracket

- Check the package contents. The mounting ear brackets shall include the items below:
  - 1x Screw Pack
  - 2x Ear Brackets

screws.

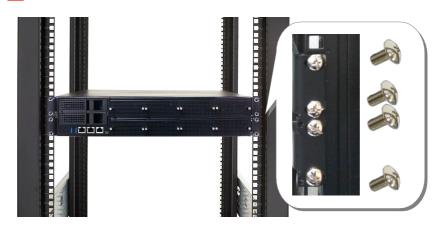




- Secure the other bracket on the other side of the chassis.
- 4. Install the chassis into the rack with the brackets fixed onto the posts using the provided screws. The actual approach you take and the needed parts for assembly will depend on the supporting accessory (shelf or rail kit) you use.







### Installing the System Using the Slide Rail Kit (with Short Mounting Ear Brackets)

 Check the package contents of the Slide Rail Kit. The kit shall include the following items:

1x pack of <u>M4X4L</u> screws (for securing the sliding rail on the system)

1x pack of <u>7.1 Round Hole</u> screws (for securing the system on the rail posts)

2 x Slide Rails

The rail consists of the following parts:

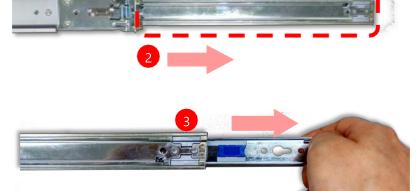
LDPE Rame TARE 7.1 ROUND HOLE





#### Attaching the Rail Brackets

- **2.** Unpack a slide rail and slide the inner channel all the way to the end.
- **3.** Stretch the bracket to the fullest.
- Remove the bracket from the Inner Rail by pushing the Release Tab on the bracket outwards while sliding it out.



5. Align the bracket to the side of the chassis and make sure the screw-holes are matched, and then secure the bracket onto the chassis with five provided <u>M4X4L</u> screws.



Align the screws with the indicated holes on the brackets as well as the screw holes on the side of the chassis.

6. Repeat Steps 2~5 to attach the bracket to the other side of the chassis.



#### **Assembling the Ear Brackets**

- Check the package contents. The supplied mounting kit shall include the items below:
  - 1x pack of screws
  - 2x Standard Ear Brackets
- Install the brackets on both sides of the system using the provided screws.



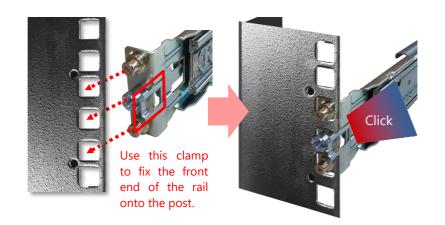


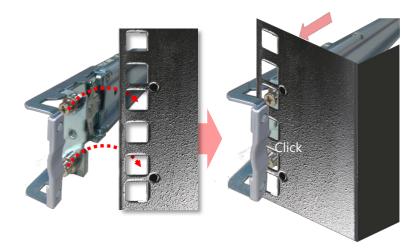


#### **Installing the Slide Rails**

Now, you shall install the slide rail assemblies onto the rack.

- 9. This slide-rail kit does NOT require screw-fixing. Simply aim at <u>3</u> available screw holes on the rack front and snap the rail front into the rack post as shown in the image below. You should hear a "click" sound once it is firmly attached.
- **10.**For the rear rack installation, slide the rail to aim and engage the bolts on the rail's rear end with the 2 available holes on the post, and the rail assembly will click into place.





#### Installing the System into the Rack

**11.** Stretch both of the inner rails out to their fullest extent. You will hear a click sound when they are fully stretched and locked.

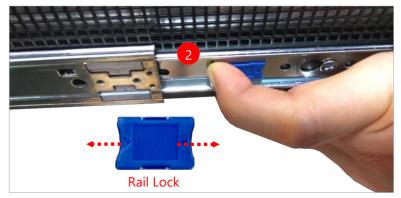


#### FX-3230 User Manual

**12.** Hold the system with its front facing you, lift the chassis and gently engage the brackets on the model while aligning them with the slide-rail assemblies as shown in the image below, and then push the system into the cabinet.

While pushing in the system, please also push and hold the Rail Lock tab on both brackets.







13. To remove the system from the rack, gently pull it outwards towards you while pushing the Release Tab on both sides of the brackets.





**Note**: Make sure that the system air vents have sufficient airflow from front to rear when the system is mounted in a rack. If the system vents are blocked, the system might be damaged due to overheat.



**Warning**: Since the system is heavy, we recommend that at least two people work together to remove the system from a rack to prevent injury, or damage to the system.

# **CHAPTER 4: BIOS SETUP**

## **Main Setup**

To enter the BIOS setup utility, simply follow the steps below:

- 1. Boot up the system.
- 2. Pressing the **<Tab>** or **<Del>** key immediately allows you to enter the Setup utility, then you will be directed to the BIOS main screen. The instructions for BIOS navigations are as below:

Control Keys	Description		
$\rightarrow \leftarrow$	select a setup screen		
↑↓	select an item/option on a setup screen		
<enter></enter>	select an item/option or enter a sub-menu		
+/-	adjust values for the selected setup item/option		
F1	display General Help screen		
F2	retrieve previous values, such as the last configured parameters during the last time you entered BIOS		
F3	load optimized default values		
F4	save configurations and exit BIOS		
<esc></esc>	exit the current screen		

BIOS Information		Set the Date. Use Tab
BIOS Vendor	American Megatrends	to switch between Date
Core Version	5.12 0.83 ×64	elements.
Compliancy	UEFI 2.5; PI 1.4	Default Ranges:
BIOS Version	FNCA6210C00006V113	Year: 2005–2099
Build Date and Time	03/13/2018 16:37:38	Months: 1–12
Access Level	Administrator	Days: dependent on month
System Date	[Sun 01/01/2017]	
System Time	[00:02:12]	++: Select Screen
		↑↓: Select Item
		Enter: Select
		+/-: Change Opt.
		F1: General Help F2: Previous Values
		F3: Optimized Defaults
		F4: Save & Exit
		ESC: Exit

(The screenshots presented in this section are for reference only)

ltem	Description		
BIOS Information	BIOS Vendor : American Megatrends Core Version : AMI Kernel version, CRB code base, X64 Compliancy : UEFI version, PI version Project Version: BIOS release version Build Date and Time: MM/DD/YYYY Access Level: Administrator / User		
System Date	To set the Date, use <b><tab></tab></b> to switch between Date elements. Default range of Year: 2005-2099 Default range of Month: 1-12 Days: dependent on Month.		
System Tine	To set the Date, use <b><tab></tab></b> to switch between Date elements.		

## **Advanced Setup**

Use  $[\rightarrow]$  or  $[\leftarrow]$  to select [Advanced] setup screen. Under this screen, you may use  $[\uparrow][\downarrow]$  to select an item you want to configure.

Main Advanced Platform Socket Server	17 American Megatrends, Inc. Mgmt Security Boot
<ul> <li>iSCSI Configuration</li> <li>Intel(R) Virtual RAID on CPU</li> <li>Trusted Computing</li> <li>Super IO Configuration</li> <li>Case Open Configuration</li> <li>LAN Boot Configuration</li> <li>Status LED Configuration</li> <li>Digital I/O Configuration</li> <li>Watch Dog Timer Configuration</li> <li>Serial Port Console Redirection</li> <li>PCI Subsystem Settings</li> <li>Network Stack Configuration</li> <li>USB Configuration</li> </ul>	Configure the iSCSI parameters.

AB

## iSCSI Configuration

Aptio Setup Advanced	Jtility – Copyright (C) 2017 American Megatrends, Inc.	
iSCSI Initiator (	Name The worldwide unique	
▶ Add an Attempt	Initiator. Only IQN format is accepted.	
▶ Delete Attempts	Range is from 4 to 223	
▶ Change Attempt O	rden	
	++: Select Screen †1: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit	
Version 2.19.1268. Copyright (C) 2017 American Megatrends, Inc. <mark>B4</mark>		
ltem	Description	
iSCSI Initiator Name	The worldwide unique name of iSCSI Initiator. Only IQN format is accept	
	The range is from 4 to 223.	

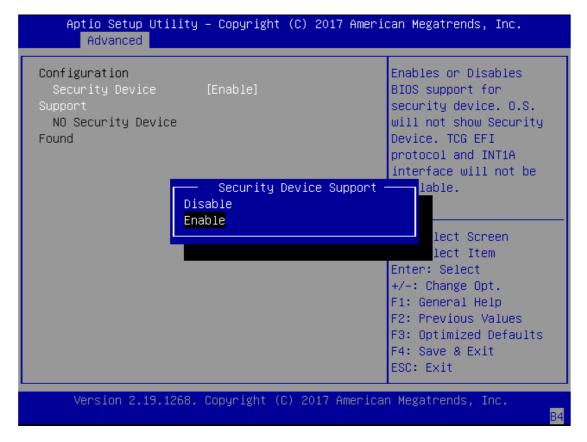
### Intel <sup>®</sup> Virtual RAID on CPU

This option allows you to check RAID volume management information supported by Intel® Virtual RAID on CPU (Intel® VROC). Press <**Enter**> access the submenu.

Aptio Setup Utility – Copyright (C) 2017 Amer Advanced	ican Megatrends, Inc.
Intel(R) VROC with VMD Technology 5.1.0.1007	
No RAID volumes on the system	
No Intel VMD Controllers on the system	
	↔+: Select Screen ↑↓: Select Item Enter: Select
	+/-: Change Opt. F1: General Help F2: Previous Values
	F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.19.1268. Copyright (C) 2017 Americ	an Megatrends, Inc.

### **Trusted Computing**

This option allows you to configure parameters regarding BIOS support for security device. Press **< Enter>** to access the submenu.



Item	Option	Description
Security Device Support	Enabled Disabled	Select "Enable "or "Disable "to turn on or off the BIOS support for Security Device. The default is "Enabled ". By disabling this function, OS will not show Security Device, and neither will TCG EFI protocol and INT1A interface be available.

#### **Trusted Computing (TPM1.2)**

Aptio Setup Utilit Advanced	y – Copyright (C) 2017 Amer.	ican Megatrends, Inc.
Configuration Security Device	[Enable]	Enables or Disables BIOS support for
Support	[]	security device. O.S.
TPM State	[Enabled]	will not show Security
Pending operation	[None]	Device. TCG EFI
Device Select	[Auto]	protocol and INT1A
		interface will not be
Ourseast Otatura Trafarana		available.
Current Status Informa TPM Enabled Status:		
TPM Active Status:		↔: Select Screen
TPM Owner Status:		14: Select Item
	Cuille	Enter: Select
		+/-: Change Opt.
		F1: General Help
		F2: Previous Values
		F3: Optimized Defaults
		F4: Save & Exit
		ESC: Exit
United in 10, 40, 4050		- Hesteducusle - Tuc

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ltem	Option	Description
Security	Enabled	Enables or disables BIOS support for security device. By
Device	Disabled	disabling this function, OS will not show Security Device.
Support	Disabica	TCG EFI protocol and INT1A interface will not be available.
	Enabled	Enables or disables Security Device.
TPM State	te	NOTE: Your computer will reboot during restart in order to
	Disabled	change State of the Device.
Donding	None	Schedules an Operation for the Security Device.
Pending		NOTE: Your computer will reboot during restart in order to
operation		change State of Security Device.
		TPM 1.2 will restrict support to TPM 1.2 devices; while TPM
Device	TPM 1.2	2.0 will restrict support to TPM 2.0 devices; Auto will support
Select	TPM 2.0	both with the default set to TPM 2.0 devices. If not found,
	Auto	TPM 1.2 devices will be enumerated.

#### **Trusted Computing (TPM2.0)**

Aptio Setup Utility Advanced	– Copyright (C) 2017 Ameria	can Megatrends, Inc.
TPM20 Device Found Vendor: NTC Firmware Version: 1.3 Security Device Support Active PCR banks	[Enable] SHA-1,SHA256	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.
Platform Hierarchy Storage Hierarchy	[Enabled] [Enabled]	<pre>++: Select Screen  t↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

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Aptio Setup Utility Advanced	– Copyright (C) 2017 Ameri	can Megatrends, Inc.
Active PCR banks	SHA-1,SHA256	TPM 1.2 will restrict
Available PCR banks	SHA-1,SHA256	support to TPM 1.2 devices, TPM 2.0 will
SHA-1 PCR Bank	[Enabled]	restrict support to TPM
SHA256 PCR Bank	[Enabled]	2.0 devices, Auto will
		support both with the
Pending operation	[None]	default set to TPM 2.0
Platform Hierarchy	[Enabled]	devices if not found,
Storage Hierarchy	[Enabled]	
Endorsement	[Enabled]	
Hierarchy		++: Select Screen
TPM2.0 UEFI Spec	[TCG_2]	↑↓: Select Item
Version		Enter: Select
Physical Presence	[1.3]	+/-: Change Opt.
Spec Version		F1: General Help
TPM 20	[TIS]	F2: Previous Values
InterfaceType		F3: Optimized Defaults
Device Select	[Auto]	/F4: Save & Exit
		ESC: Exit
Version 2.19.1268.	Copyright (C) 2017 America	n Megatrends, Inc.

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ltem	Option	Description
Security Device Support	Enabled Disabled	Enables or disables BIOS support for security device. By disabling this function, OS will not show Security Device. TCG EFI protocol and INT1A interface will not be available.
SHA-1 PCR Bank	Enabled Disabled	Enables or disables SHA-1 PCR Bank.
SHA256 PCR Bank	Enabled Disabled	Enables or disables SHA256 PCR Bank.
Pending operation	<mark>None</mark> TPM Clear	Schedules an Operation for the Security Device. NOTE: Your computer will reboot during restart in order to change State of Security Device.
Platform Hierarchy	Enabled Disabled	Enables or disables Platform Hierarchy.
Storage Hierarchy	Enabled Disabled	Enables or disables Storage Hierarchy.
Endorsement Hierarchy	Enabled Disabled	Enables or disables Endorsement Hierarchy.
TPM2.0 UEFI Spec Version	TCG_1_2 TCG_2	Select the TCG2 Spec Version, <b>TCG_1_2</b> : Supports the Compatible mode for Win8/Win10 <b>TCG_2</b> : Supports new TCG2 protocol and event format for Win10 or later.
Physical Presence Spec Version	1.2 1.3	Select to tell OS to support PPI Spec Version 1.2 or 1.3. NOTE: Some HCK tests might not support 1.3.
TPM 20 InterfaceType	TIS	Select <b>TPM 20 Device</b> for the Communication Interface.
Device Select	TPM 1.2 TPM 2.0 Auto	<b>TPM 1.2</b> will restrict support to TPM 1.2 devices; while <b>TPM2.0</b> will restrict support to TPM 2.0 devices; <b>Auto</b> will support both with the default set to TPM 2.0 devices. If not found, TPM 1.2 devices will be enumerated.

#### Trusted Computing (PTT Enable)

Aptio Setup Utility Advanced	– Copyright (C) 2017 Americ	can Megatrends, Inc.
TPM20 Device Found Vendor: INTC Firmware Version: 4.0 Security Device Support Active PCR banks Available PCR banks	[Enable] SHA-1,SHA256	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.
SHA-1 PCR Bank SHA256 PCR Bank Pending operation Platform Hierarchy Storage Hierarchy Endorsement Hierarchy	[Enabled] [Enabled]	<pre>→+: Select Screen  ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

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Aptio Setup Utility Advanced	) – Copyright (C) 2017 Amer:	ican Megatrends, Inc.
Active PCR banks		TPM 1.2 will restrict
Available PCR banks	SHA-1,SHA256	support to TPM 1.2 devices, TPM 2.0 will
SHA-1 PCR Bank	[Enabled]	restrict support to TPM
SHA256 PCR Bank	[Enabled]	2.0 devices, Auto will support both with the
Pending operation	[None]	default set to TPM 2.0
Platform Hierarchy		devices if not found,
Storage Hierarchy	[Enabled]	
Endorsement	[Enabled]	
Hierarchy		↔+: Select Screen
TPM2.0 UEFI Spec	[TCG_2]	î∔: Select Item
Version		Enter: Select
	[1.3]	+/-: Change Opt.
Spec Version		F1: General Help
TPM 20	[CRB]	F2: Previous Values
InterfaceType		F3: Optimized Defaults
Device Select	[Auto]	▼ F4: Save & Exit
		ESC: Exit
therefore 0.40.4000	Copyright (C) 2017 America	The Tree

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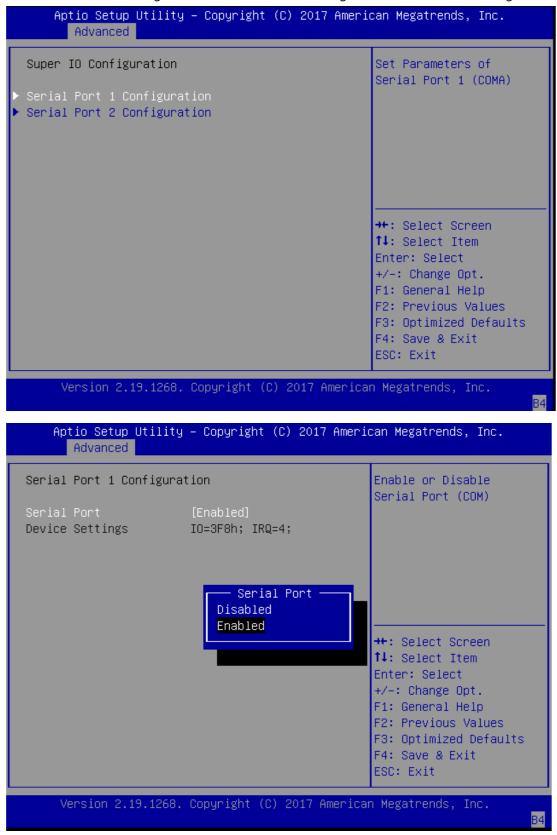
ltem	Option	Description
Security Device Support	Enabled Disabled	Enables or disables BIOS support for security device. By disabling this function, OS will not show Security Device. TCG EFI protocol and INT1A interface will not be available.
SHA-1 PCR Bank	Enabled Disabled	Enables or disables SHA-1 PCR Bank.
SHA256 PCR Bank	Enabled Disabled	Enables or disables SHA256 PCR Bank.
Pending operation	None TPM Clear	Schedules an Operation for the Security Device. NOTE: Your computer will reboot during restart in order to change State of Security Device.
Platform Hierarchy	Enabled Disabled	Enables or disables Platform Hierarchy.
Storage Hierarchy	Enabled Disabled	Enables or disables Storage Hierarchy.
Endorsement Hierarchy	Enabled Disabled	Enables or disables Endorsement Hierarchy.
TPM2.0 UEFI Spec Version	TCG_1_2 TCG_2	Select the TCG2 Spec Version, <b>TCG_1_2</b> : Supports the Compatible mode for Win8/Win10 <b>TCG_2</b> : Supports new TCG2 protocol and event format for Win10 or later.
Physical Presence Spec Version	1.2 1.3	Select to tell OS to support PPI Spec Version 1.2 or 1.3. NOTE: Some HCK tests might not support 1.3.
TPM 20 InterfaceType	CRB	Select the <b>CRB</b> (Communication Interface) for TPM 20 Device.
Device Select	TPM 1.2 TPM 2.0 Auto	<b>TPM 1.2</b> will restrict support to TPM 1.2 devices; while <b>TPM2.0</b> will restrict support to TPM 2.0 devices; <b>Auto</b> will support both with the default set to TPM 2.0 devices. If not found, TPM 1.2 devices will be enumerated.

### **Super IO Configuration**

This option allows you to configure parameters about Super IO Chip. Press **< Enter>** to access the submenu.

#### **Serial Port Configuration**

Select "Serial Port 1 Configuration" or "Serial Port 2 Configuration" to enter sub setting screen.



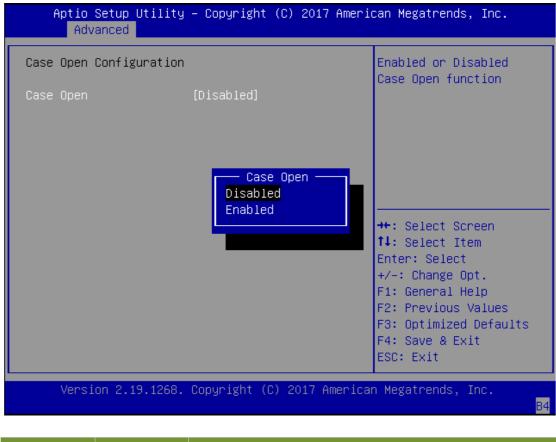
ltem	Option	Description
Serial Port	Enabled Disabled	Enables or disables Serial Port 1.
Device Settings	NA	IO=3F8h; IRQ = 4

	Setup Utility vanced	– Copyright (C) 2017 Ameri	can Megatrends, Inc.
Serial Po	rt 2 Configurat	tion	Enable or Disable Serial Port (COM)
Serial Po Device Se		[Enabled] IO=2F8h; IRQ=3;	
		Serial Port — Disabled Enabled	<pre>→+: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values</pre>
			F3: Optimized Defaults F4: Save & Exit ESC: Exit
Vers.	ion 2.19.1268.	Copyright (C) 2017 America	n Megatrends, Inc. <mark>B4</mark>
ltem	Ontion	Doccr	intion

Item	Option	Description
Serial Port	Enabled Disabled	Enables or disables Serial Port 2
Device Settings	NA	IO=2F8h; IRQ = 3

#### **Case Open Configuration**

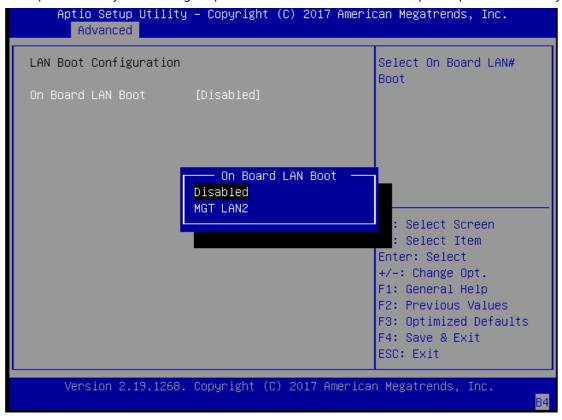
If with the case's support, enabling this option will have the system sound when someone opens the case of this system, which is considered against your organization's policy.



	ltem	Option	Description
	Case Open	Enabled	Enables or disables Case Open function
Disabled	Enables of disubles case open function		

### **LAN Boot Configuration**

This option allows you to configure parameters about LAN Boot. The options provided will vary by SKU.



## **Status LED Configuration**

This option allows you to change the color of status LED.

Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc. Advanced			
Status LED Configu	Status LED Configuration		
Status LED	[DARK]	++: Select Screen †↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values	
		F3: Optimized Defaults F4: Save & Exit ESC: Exit	
Version 2.19.	1268. Copyright (C) 2017	American Megatrends, Inc.	
ltem	Option	Description	
	DARK		
Status LED	GREEN	Configures Status LED color	

RED

## **Digital IO Configuration**

This option allows you to configure parameters about Digital IO pins.

Advanced	017 American Megatrends, Inc.
Digital I/O Configuration	Configure Digital I/O Pin 5.
Digital I/O Output 1 [Output High]	
Digital I/O Output 2 [Output High]	
Digital I/O Output 3 [Output High]	
Digital I/O Output 4 [Output High]	
	++: Select Screen
	↑↓: Select Item
	Enter: Select
	+/-: Change Opt.
	F1: General Help
	F2: Previous Values
	F3: Optimized Defaults
	F4: Save & Exit
	ESC: Exit

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ltem	Option	Description	
Digital I/O Output 1	Output High	Configure Digital 1/0 Pin5	
Digital I/O Output 1	Output Low	Configure Digital I/O Pin5	
Digital I/O Output 2	Output High	Configure Digital 1/0 Pinc	
Digital I/O Output 2	Output Low	Configure Digital I/O Pin6	
Digital I/O Output 3	Output High	Configure Digital I/O Pin7	
	Output Low		
Disital 1/0 Outsut 1	Output High	Configure Digital 1/0 Ding	
Digital I/O Output 4	Output Low	Configure Digital I/O Pin8	

## Watch Dog Timer Configuration

This option allows you to enable or disable the watchdog timer function.

Aptio Setup Utility – Co Advanced	oyright (C) 2017 Americ	can Megatrends, Inc.
Watch Dog Timer Configuration	n	Enabled or Disabled Watch Dog Timer function
Watch Dog Timer [Dis	abled]	
		<pre>++: Select Screen t4: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
Version 2.19.1268. Copy	right (C) 2017 Americar	n Megatrends, Inc. B4

ltem	Option	Description
Watch Dog Timer	Enabled	Enables or disables Watch Dog Timer function
	Disabled	enables of disables watch bog filler function

### Watch Dog Timer Configuration

This option allows you to configure parameters about serial port console redirection. Press < Enter> to

access the submenu.	
Aptio Setup Utility – Copyright (C) 2017 Ame Advanced	rican Megatrends, Inc.
COMO Console Redirection [Enabled] ▶ Console Redirection Settings	Console Redirection Enable or Disable.
	<pre>++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

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ltem	Option	Description
COM0	Enabled	Enables or disables Console Redirection
<b>Console Redirection</b>	Disabled	Enables of disables console Redirection

#### **Console Redirection Settings**

These settings specify how the host computer and the remote computer will exchange data. Both computers should have the same or compatible settings.

Advanced COMO Console Redirection Set Terminal Type	[VT100+] [115200] [8] [VT100 VT100+ VT-UTF8 ANSI [Di	<ul> <li>Emulation: ANSI: Extended ASCII char set. VT100: ASCII char set. VT100+: Extends VT100 to support color, function keys, etc. VT-UTF8: Uses UTF8 encoding to map Unicode</li> <li>++: Select Screen fl: Select Item Enter: Select</li> </ul>
Support		
Resolution 100x31 Legacy OS Redirection Resolution	[01 [80x24]	+/-: Change Opt. F1: General Help F2: Previous Values
Putty KeyPad	[VT100]	▼ F3: Optimized Defaults ▼ F4: Save & Exit ESC: Exit

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ltem	Option	Description
Terminal Type	VT100 VT100+ VT-UTF8 ANSI	<ul> <li>VT100: ASCII char set</li> <li>VT100+:Extends VT100 to support color, function keys, etc.</li> <li>VT-UTF8:Uses UTF8 encoding to map Unicode chars onto 1 or more bytes</li> <li>ANSI: Extended ASCII char set</li> </ul>
Bits per second	9600 19200 38400 57600 115200	Selects serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.
Data Bits	7 8	Data Bits
Parity	None Even Odd Mark Space	A parity bit can be sent with the data bits to detect some transmission errors.
Stop Bits	1 2	Indicates the end of a serial data packet.
Flow Control	None	Flow Control can prevent data loss from buffer overflow.

	Hardware RTS/CTS	
VT-UTF8 Combo Key Support	Disabled Enabled	Enables VT-UTF8 Combination Key Support for ANSI/VT100 terminals
Recorder Mode	Disabled Enabled	With this mode enabled, only text will be sent. This is to capture Terminal data.
Resolution 100x31	Disabled Enabled	Enables or disables extended terminal resolution
Legacy OS Redirection Resolution	<mark>80x24</mark> 80x25	On Legacy OS, the Number of Rows and Columns supported redirection.
Putty KeyPad	VT100 LINUX XTERM86 SCO ESCN VT400	Selects FunctionKey and KeyPad on Putty.
Redirection After BIOS POST	Always Enable BootLoader	When <b>Bootloader</b> is selected, Legacy Console Redirection is disabled before booting to legacy OS. When <b>Always Enable</b> is selected, then Legacy Console Redirection is enabled for legacy OS. Default setting for this option is set to <b>Always Enable</b> .

### **PCI Subsystem Settings**

This option allows you to change the PCI, PCI-X and PCI Express settings.

5.01.12 gs: Disable]	Enables or Disables 64bit capable Devices to be Decoded in Above 4G Address Space (Only
Disable]	AC QUESTION OF MERSING CA
	if System Supports 64
Disable]	bit PCI Decoding).
	<pre> ++: Select Screen  1↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

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Item	Option	Description
Above 4G Decoding	Disabled Enabled	Enables or disables 64bit capable Devices to be Decoded in Above 4G Address Space (Only if System Supports 64 bit PCI Decoding)
SR-IOV	Disabled	If the system has SR-IOV capable PCIe Devices, this option
Support	Enabled	enables or disables Single Root IO Virtualization Support.

## Network Stack Configuration

This option enables or disables UEFI network stack.

Aptio Setup Ut: Advanced	ility – Copyright (C) 201	.7 American Megatrends, Inc.
Network Stack	[Disabled]	Enable/Disable UEFI Network Stack
		<pre>++: Select Screen f↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

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ltem	Option	Description
Network	Disabled	Enables or disables UEFI Network Stack
Stack	Enabled	
Ipv4 PXE	Disabled	Enables Ipv4 PXE Boot Support. If IPV4 is disabled, PXE boot option
Support	Enabled	will not be created.
Ipv4 HTTP	Disabled	Enables Ipv4 HTTP Boot Support. If IPV4 is disabled, HTTP boot option
Support	Enabled	will not be created.
Ipv6 PXE	Disabled	Enables Ipv6 PXE Boot Support. If IPV6 is disabled, PXE boot option
Support	Enabled	will not be created.
lpv6 HTTP	Disabled	Enables Ipv6 HTTP Boot Support. If IPV6 is disabled, HTTP boot option
Support	Enabled	will not be created.
Support	LINDICO	
PXE boot	0	Wait time to press <b><esc></esc></b> key to abort the PXE boot
wait time	v	Walt time to press <b>LBC</b> , key to abort the rike boot
Media detect count	1	Number of times the presence of media will be checked

## **CSM Configuration**

This option allows you to enable or disable ROM execution settings.

Aptio Setup Utilit Advanced	y – Copyright (C) 2017 Amer	rican Megatrends, Inc.
Compatibility Support (	Module Configuration	Enable/Disable CSM Support.
CSM Support	[Enabled]	
CSM16 Module Version	07.81	
Option ROM execution		
Network Storage Video Other PCI devices	[Legacy] [Legacy] [Legacy] [Legacy]	<pre>→+: Select Screen 1↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
Vension 2 40 420	Conuright (C) 2017 Americ	Nogotnondo Tro

/ersion 2.19.1268. Copyright (C) 2017 American Megatrends, Ind

ltem	Option	Description
CSM Support	Disabled Enabled	Enables or disables CSM Support
Network	Do Not Launch UEFI Legacy	Controls the execution of UEFI and Legacy PXE OpROM
Storage	Do Not Launch UEFI <mark>Legacy</mark>	Controls the execution of UEFI and Legacy Storage OpROM
Video	Do Not Launch UEFI Legacy	Controls the execution of UEFI and Legacy Video OpROM
Other PCI device	Do Not Launch UEFI Legacy	Determines OpROM execution policy for devices other than Network, Storage, or Video

## **CSM Configuration**

This option allows you to change USB configuration parameters.

Aptio Setup Utility Advanced	– Copyright (C) 2017 Americ	can Megatrends, Inc.
USB Configuration	La constante de	Enables Legacy USB support. AUTO option
USB Module Version	17	disables legacy support if no USB devices are
USB Controllers: 1 XHCI		connected. DISABLE option will keep USB
USB Devices: 1 Drive, 1 Keyboa	rd	devices available only for EFI applications.
Legacy USB Support XHCI Hand-off	[Enabled] [Enabled]	→+: Select Screen
USB Mass Storage Driver Support		t↓: Select Item Enter: Select +/-: Change Opt.
USB hardware delays and time–outs:		F1: General Help F2: Previous Values
USB transfer time-out Device reset time-out	1995	F3: Optimized Defaults F4: Save & Exit ESC: Exit

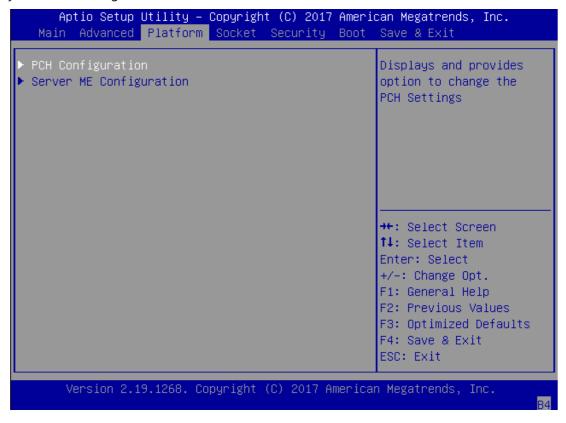
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Β4

Item	Option	Description
Legacy USB Support	<mark>Enabled</mark> Disabled Auto	Enables Legacy USB support. <b>Auto</b> option disables legacy support if no USB devices are connected; <b>Disabled</b> option will keep USB devices available only for EFI applications.
XHCI Hand-off	Enabled Disabled	This is a workaround for OSes without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.
USB Mass Storage Driver Support	Enabled Disabled	Enables or disables USB Mass Storage Driver Support.
USB transfer time-out	1 sec 5 sec 10 sec <mark>20 sec</mark>	The time-out value for Control, Bulk, and Interrupt transfers
Device reset time-out	1 sec 5 sec 10 sec <mark>20 sec</mark>	USB mass storage device Start Unit command time-out
Device power-up delay	<mark>Auto</mark> Manual	Maximum time the device will take before it properly reports itself to the Host Controller. <b>Auto</b> uses default value: for a Root port, it is 100 ms, for a Hub port the delay is taken from Hub descriptor.

## **Platform Setup**

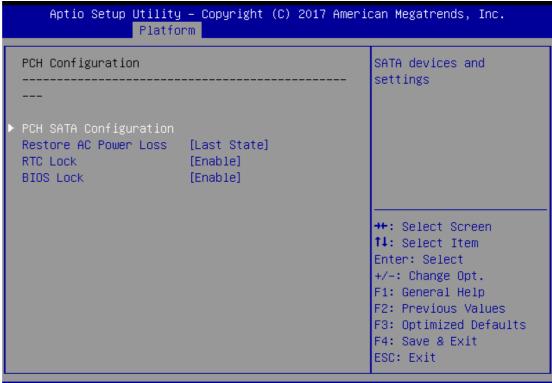
Use  $[\rightarrow]$  or  $[\leftarrow]$  to select [Platform] setup screen. Under this screen, you may use  $[\uparrow][\downarrow]$  to select an item you want to configure.



ltem	Option	Description
PCH Configuration	None	Displays and provides option to change the PCH Settings
Server ME Configuration	None	Configure Server ME Technology Parameters

### **PCH Configuration**

This option displays and provides options to change the PCH Settings.



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ltem	Option	Description
PCH SATA Configuration	None	SATA devices and settings
Restore AC Power Loss	Power ON Power Off Last State	Select S0/S5 for ACPI state after a G3
RTC Lock	Disabled Enabled	Enabling this feature will lock bytes 38h-3Fh in the lower/upper 128-byte bank of RTC RAM
BIOS Lock	Disabled Enabled	Enables or disables the PCH BIOS Lock Enable feature.

## PCH SATA Configuration

This option allows you to configure SATA devices related options.

Aptio Setup Utility Platfo	ı — Copyright (C) 2017 Ameri rm	ican Megatrends, Inc.
PCH SATA Configuration 		Enable or Disable SATA Controller
SATA Controller Configure SATA as Support Aggressive Link Power Management	[AHCI]	
SATA Port O SATA Port 1 SATA Port 2 SATA Port 3 SATA Port 4	[Not Installed] [Not Installed] [Not Installed] [Not Installed] [Not Installed]	<pre>→+: Select Screen 1↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

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ltem	Option	Description	
SATA Controller	Disabled	Enables or disables SATA Controller	
SATA COntroller	Enabled		
Configure SATA oc	AHCI	This will configure SATA as <b>BAID</b> or <b>AHCI</b>	
Configure SATA as	RAID	This will configure SATA as <b>RAID</b> or <b>AHCI</b>	
Support Aggressive Link Power	Disabled	Enables or disables SALP	
Management	Enabled		

## Server ME Configuration

This option configures server ME technology parameters.

General ME Configuratio	on	Enable/disable Platform
	0A:4.0.4.288	Trusted Technology
Version Realway Firmwore	N ZA	(PTT) support
Backup Firmware Version	NZA	
Recovery Firmware Version	0A:4.0.4.288	
ME Firmware Status #1	0x000F0245	
ME Firmware Status #2		
Current State	•	
Error Code	No Error	++: Select Screen
······································	N/A	↑↓: Select Item
PTT Support	[Disable]	Enter: Select
		+/-: Change Opt.
		F1: General Help
		F2: Previous Values
		F3: Optimized Defaults
		F4: Save & Exit
		ESC: Exit

Item	Option	Description
	Disable	Enables or disables Platform Trusted Technology (PTT)
PTT Support	Enable	support.

# Socket Setup

Use  $[\rightarrow]$  or  $[\leftarrow]$  to select [Socket] setup screen. Under this screen, you may use  $[\uparrow][\downarrow]$  to select an item you want to configure.

Aptio Setup Utility – Copyright (C) 2017 Am Main Advanced Platform <mark>Socket</mark> Security Bo	
<ul> <li>Processor Configuration</li> <li>IIO Configuration</li> <li>Advanced Power Management Configuration Numa [Enable]</li> </ul>	Displays and provides option to change the Processor Settings
	<pre>++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

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ltem	Option	Description
Processor Configuration	None	Displays and provides option to change the Processor Settings
IIO Configuration	None	Displays and provides option to change the IIO Settings
Advanced Power Management Configuration		Displays and provides option to change the Power Management Settings
Num Enabled		Displays and provides option to change the Power Management Settings

## **Processor Configuration**

In Processor Configuration, you can change the processor settings and view the current parameters.

Processor Configuration  Processor BSP	 ] 	Enables Hyper Threading
		(Software Method to
Revision Processor Socket Processor ID Processor Frequency Processor Max Ratio Processor Min Ratio Microcode Revision L1 Cache RAM L2 Cache RAM L3 Cache RAM Processor 0 Version Processor 1 Version	50654 - SKX MO Socket 0 Socket 1 00050654*   00050654 2.100GHz   2.100GHz 15H   15H 08H   08H 0200001E 64KB   64KB 1024KB   1024KB 16896KB   16896KB Intel(R) Xeon(R) Silver 4116 CPU @ 2.10GHz Intel(R) Xeon(R) Silver 4116 CPU @ 2.10GHz	<pre>Enable/Disable Logical Processor threads.  ++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults  F4: Save &amp; Exit ESC: Exit</pre>
Version 2.19.1268.	. Copyright (C) 2017 Amer:	ican Megatrends, Inc.
Aptio Setup Utility	y – Copyright (C) 2017 Ame Socket	erican Megatrends, Inc.
L1 Cache RAM L2 Cache RAM L3 Cache RAM Processor O Version Processor 1 Version	64KB   64KB 1024KB   1024KB 16896KB   16896KB Intel(R) Xeon(R) Silver 4116 CPU @ 2.10GHz Intel(R) Xeon(R) Silver 4116 CPU @ 2.10GHz	Enable/disable AES-NI support
Hyper-Threading [ALL] Execute Disable Bit Enable Intel(R) TXT VMX Enable SMX Hardware Prefetcher Adjacent Cache Prefetch Extended APIC AES-NI	[Enable] [Enable] [Disable] [Enable] [Enable] [Enable] [Disable] [Enable]	<ul> <li>++: Select Screen</li> <li>↑↓: Select Item</li> <li>Enter: Select</li> <li>+/-: Change Opt.</li> <li>F1: General Help</li> <li>F2: Previous Values</li> <li>F3: Optimized Defaults</li> <li>F4: Save &amp; Exit</li> <li>ESC: Exit</li> </ul>

ltem	Option	Description	
Hyper-Threading	Disabled	Enables Hyper Threading (Software Method to Enable/Disable Logica	
[ALL]	Enabled	Processor threads.	
Execute Disable	Disabled	When disabled, it forces the XD feature flag to always return 0.	
Bit	Enabled	when disabled, it forces the XD feature hay to always feture 0.	
Enable Intel® TXT	Disabled	Enables Intel(R) TXT	
	Enabled		
VMX	Disabled	Enables the Vanderpool Technology, which takes effect after reboot.	
VIVIA	Enabled	enables the validerpoor rechnology, which takes effect after reboot.	
Enable SMX	Disabled	Enables Safer Mode Extensions	
	Enabled		
Hardware	Disabled	= MLC Streamer Prefetcher (MSR 1A4h Bit[0])	
Prefetcher	Enabled		
Adjacent Cache	Disabled	= MLC Spatial Prefetcher (MSR 1A4h Bit[1])	
Prefetcher	Enabled		
Extended APIC	Disabled	Enables or disables extended APIC support	
	Enabled	chaples of disables extended Aric support	
AES-NI	Disabled	Enables or disables AES-NI support	
AED-INI	Enabled	Linables of disables AES-INI support	

## **IIO Configuration**

In Processor Configuration, you can change the processor settings and view the current parameters.

IIO Configuration 		Press <enter> to bring up the Intel® VT for Directed I/O (VT–d) Configuration menu.</enter>
SocketO Configuration Socket1 Configuration Intel® VT for Directed	d I∕O (VT-d)	
		++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.19.126	3. Copyright (C	) 2017 American Megatrends, Inc.
ltem	Option	Description

Item	Option	Description
Socket0 Configuration	None	None
Socket1 Configuration	None	None
Intel <sup>®</sup> VT for Directed	Nono	Press < Enter> to bring up the Intel? VT for
I/O (VT-d)	None	Directed I/O (VT-d) Configuration menu.

### Socket0 Configuration

Enter to configure the settings related to PCI Express ports under Socket0.

Aptio Setup Utility — Copyright (C) 201 Socket	.7 American Megatrends, Inc.
IOUO (IIO PCIe Br1) [x8x8] IOU1 (IIO PCIe Br2) [x8x8] Socket O PcieBr1D00FO - Port 1A Socket O PcieBr1D02FO - Port 1C Socket O PcieBr2D00FO - Port 2A Socket O PcieBr2D02FO - Port 2C	Settings related to PCI Express PortS (0/1A/1B/1C/1D/2A/2B/2C/ 2D/3A/3B/3C/3D/4A/5A)
	<pre> ++: Select Screen  f↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

B4

Option	Description
None	Settings related to PCI Express Port 1A
None	Settings related to PCI Express Port 1C
None	Settings related to PCI Express Port 2A
None	Settings related to PCI Express Port 2C
	None None None

### Socket1 Configuration

Enter to configure the settings related to PCI Express ports under Socket1.

Aptio Setup Utility – Copyright (C) 2017 Socket	American Megatrends, Inc.
IOUO (IIO PCIe Br1) [x8x8] IOU1 (IIO PCIe Br2) [x8x8] IOU2 (IIO PCIe Br3) [x8x8] Socket 1 PcieBr1D00F0 - Port 1A Socket 1 PcieBr1D02F0 - Port 1C Socket 1 PcieBr2D00F0 - Port 2A Socket 1 PcieBr2D02F0 - Port 2C Socket 1 PcieBr3D00F0 - Port 3A Socket 1 PcieBr3D02F0 - Port 3C	Settings related to PCI Express PortS (0/1A/1B/1C/1D/2A/2B/2C/ 2D/3A/3B/3C/3D/4A/5A)
	<pre>++: Select Screen f↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

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Β4

Item	Option	Description
Socket 1	None	Settings related to PCI Express Port 1A
PcieBr1D00F0	Hone	
Socket 1	None	Settings related to PCI Express Port 1C
PcieBr1D02F0	None	Settings related to FCI Express Fort IC
Socket 1	None	Settings related to PCI Express Port 2A
PcieBr2D00F0	None	Settings related to PCI Express Port 2A
Socket 1	None	Sattings related to DCI Express Dart 20
PcieBr2D02F0	None	Settings related to PCI Express Port 2C
Socket 1	None	Sattings related to DCI Express Dort 24
PcieBr3D00F0	None	Settings related to PCI Express Port 3A
Socket 1	Nene	Sattings related to DCI Everage Dart 20
PcieBr3D02F0	None	Settings related to PCI Express Port 3C

## Intel VT for Directed IO (VT-d)

Enter to configure the settings related to Intel® VT for Directed IO (VT-d).

Aptio Setup Utility	– Copyright (C) 2017 Ameri Socket	.can Megatrends, Inc.
Intel® VT for Directed  Intel® VT for Directed I/O (VT-d)		Enable/Disable Intel® Virtualization Technology for Directed I/O (VT-d) by reporting the I/O device assignment to VMM through DMAR ACPI Tables.
		<pre>++: Select Screen f↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

ltem	Option	Description
Intel® VT for Directed I/O (VT-d)	<mark>Disabled</mark> Enabled	Press <b><enter></enter></b> to bring up the Intel? VT for Directed I/O (VT-d) Configuration menu.

## Advanced Power Management Configuration

This option allows you to modify the Power Management related settings and displays the current parameters.

Aptio Setup Utility – Copyright (C) 2017 Ar Socket	merican Megatrends, inc.
Advanced Power Management Configuration  CPU P State Control CPU C State Control	P State Control Configuration Sub Menu, include Turbo, XE and etc.
	<pre>++: Select Screen 1↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

ltem	Option	Description	
CPU P State	Nono	P State Control Configuration Sub Menu, include	
Control	None	Turbo, XE and etc.	
CPU C State Control	None	CPU C State setting	

#### **CPU P State Control**

Aptio Setup Utility	– Copyright (C) 2017 Ameri Socket	can Megatrends, Inc.
CPU P State Control		Enable/Disable EIST (P-States)
SpeedStep (Pstates)	[Disable]	
		→++: Select Screen
		↑↓: Select Item
		Enter: Select +∕–: Change Opt.
		F1: General Help
		F2: Previous Values
		F3: Optimized Defaults
		F4: Save & Exit ESC: Exit
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ltem	Option	Description
SpeedStep(Pstates)	Disabled	Enables or disables EIST (P-States)
SpeedStep(Fstates)	Enabled	

#### **CPU C State Control**

Aptio Setup Utilit	y – Copyright (C) 2017 Amer Socket	ican Megatrends, Inc.
CPU C State Control		Autonomous Core C-State Control
Autonomous Core C–State	[Disable]	
CPU C6 report Enhanced Halt State (C1E)		
		<pre>++: Select Screen ++: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit</pre>
Vancian 2 18 1968	. Copyright (C) 2017 Americ	ESC: Exit

ltem	Option	Description	
Autonomous Core	Disabled	Autonomous Core C-State Control	
C-State	Enabled	Autonomous core C-state control	
	Disabled	Enchles or displace CDU CC(ACDU C2) report to OS	
CPU C6 report	Enabled	Enables or disables CPU C6(ACPI C3) report to OS	
Enhanced Halt	Disabled	Core C15 outo promotion Control Takes effort often web out	
State (C1E)	Enabled	Core C1E auto promotion Control. Takes effect after rebo	

## Server Mgmt

Use  $[\rightarrow]$  or  $[\leftarrow]$  to select [Server Mgmt] setup screen. Under this screen, you may use  $[\uparrow][\downarrow]$  to select an item you want to configure.

	) – Copyright (C) 2018 A orm Socket Server Mgmt	merican Megatrends, Inc. Security Boot
BMC Support Wait For BMC FRB-2 Timer FRB-2 Timer timeout FRB-2 Timer Policy OS Watchdog Timer OS Wtd Timer Timeout OS Wtd Timer Policy System Event Log BMC network configurati	[Do Nothing] [Disabled] [10 minutes] [Reset]	Enable/Disable interfaces to communicate with BMC
<ul> <li>View System Event Log BMC Warm Reset</li> </ul>		<pre>++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

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AR

Item	Option	Description
BMC Support	Enabled Disabled	Enable or disables interfaces to communicate with BMC.
Wait For BMC	Enabled Disabled	Wait For BMC response for specified time out. In PILOTII, BMC starts at the same time when BIOS starts during AC power ON. It takes around 30 seconds to initialize Host to BMC interfaces.
FRB-2 Timer	Enabled Disabled	Enables or disables FRB-2 timer (POST timer).
FRB-2 Timer timeout	3 minutes 4 minutes 5 minutes <mark>6 minutes</mark>	Enter value Between 3 to 6 min for FRB-2 Timer Expiration value.
FRB-2 Timer Policy	Do Nothing Reset Power Down Power Cycle	Configure how the system should respond if the FRB-2 Timer expires. Not available if FRB-2 Timer is disabled.
OS Watchdog	Enabled Disabled	If enabled, it starts a BIOS timer which can only be shut off by Management Software after the OS loads. It also helps verify that

### Chapter 4: BIOS Setup

Timer		the OS is successfully loaded or follows the OS Boot Watchdog Timer policy.
OS Wtd Timer Timeout	5 minutes <mark>10 minutes</mark> 15 minutes 20 minutes	Configure the length of the OS Boot Watchdog Timer. Not available if OS Boot Watchdog Timer is disabled.
OS Wtd Timer Policy	Do Nothing Reset Power Down Power Cycle	Configure how the system should respond if the OS Boot Watchdog Timer expires. Not available if OS Boot Watchdog Timer is disabled.
System Event Log	NA	Press <b><enter></enter></b> to change the SEL event log configuration.
BMC network configuration	NA	Configure BMC network parameters.
View System Event Log	NA	Press <b><enter></enter></b> to view the System Event Log Records.
BMC Warm Reset	NA	Press <b><enter></enter></b> to do Warm Reset BMC.

## System Event Log

Use this option to change the SEL event log configuration.

Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc. Server Mgmt			
Enabling/Disabling Op	tions	Change this to enable	
SEL Components	[Enabled]	or disable all features of System Event Logging	
Erasing Settings		during boot.	
Enase SEL	[No]		
When SEL is Full	[Do Nothing]		
NOTE: All values char effect	ged here do not take		
until computer	is restarted.		
		↔+: Select Screen	
		↑↓: Select Item	
		Enter: Select	
		+/-: Change Opt.	
		F1: General Help	
		F2: Previous Values	
		F3: Optimized Defaults	
		F4: Save & Exit	
		ESC: Exit	
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AB

ltem	Option	Description
SEL Components	Disabled	Enables or disables all features of System Event
SEL Components	Enabled	Logging during boot.
	NO	
Erase SEL	Yes, On next reset	Choose options for erasing SEL.
	Yes, On every reset	
	Do Nothing Erase	
When SEL is Full	Immediately	Choose options for reactions to a full SEL.

## **BMC network configuration**

This option allows you to configure BMC network parameters.

Aptio Setup Utili	ty – Copyright (C) 2017 A Server Mgmt	
BMC network configu ****	Select to configure LAN	
Configure IPV4 suppor		channel parameters statically or
******	dynamically(by BIOS or BMC). Unspecified	
Lan channel 1		option will not modify
Configuration	[Unspecified]	any BMC network
Address source		parameters during BIOS
Current	StaticAddress	
Configuration		
Address source		++: Select Screen
Station IP address	192.168.0.100	<b>1↓:</b> Select Item
Subnet mask	255.255.255.0	Enter: Select
Station MAC address	02-0c-63-77-de-98	+/-: Change Opt.
Router IP address	192.168.0.1	F1: General Help
Router MAC address	00-00-00-00-00	F2: Previous Values
		F3: Optimized Defaults
		F4: Save & Exit
		ESC: Exit
		ESU: EXIL

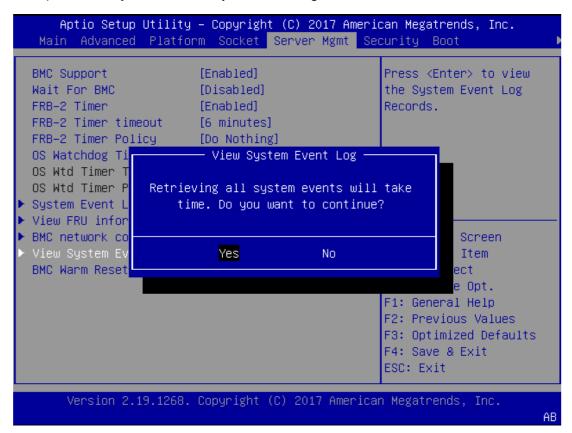
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ltem	Option	Description
Configuration Address source	Unspecified Static DynamicBmcDhcp	Select to configure LAN channel parameters statically or dynamically (by BIOS or BMC). The <b>unspecified</b> option will not modify any BMC network parameters during BIOS phase.

AB

#### **View System Event Log**

This option allows you to view the System Event Log Records.



# Security

Use  $[\leftarrow] / [\rightarrow]$  to select [Security] setup screen. Under this screen, you may use  $[\uparrow] [\downarrow]$  to select an item you would like to configure.

Aptio Setup Utility — Copyright (C) 2017 Main Advanced Platform Socket <mark>Security</mark>	
Password Description	Set Administrator Password
If ONLY the Administrator's password is set, then this only limits access to Setup and is only asked for when entering Setup. If ONLY the User's password is set, then this is a power on password and must be entered to boot or enter Setup. In Setup the User will have Administrator rights.	5
The password length must be in the following range:	++: Select Screen
Minimum length 3	↑↓: Select Item
Maximum length 20	Enter: Select
Administrator Password User Password	+/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults
▶ Secure Boot	F4: Save & Exit ESC: Exit

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ltem	Description		
Administrator	Set the administrator password. Once set, then this only limits access to		
Password	Setup and is only asked for when entering Setup.		
	Set the user password. Once set, then this is a power-on password and		
User Password	must be entered to boot or enter Setup. In Setup, the user will have		
	Administrator rights.		
Secure Boot	Allows you to customize Secure Boot settings.		

## Secure Boot

This option allows you to customize Secure Boot settings.

Aptio Setup Utility – Copyright (C) 2017 American Megatrends, Inc. Security			
System Mode Secure Boot Vendor Keys Attempt Secure Boot Secure Boot Mode ► Key Management	Setup Not Active Active	Secure Boot activated when Platform Key(PK) is enrolled, System mode is User/Deployed, and CSM function is disabled ++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values	
		F3: Optimized Defaults F4: Save & Exit ESC: Exit	

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ltem	Option	Description
Attempt Secure Boot	Disabled Enabled	Secure Boot is activated when Platform Key(PK) is enrolled, System mode is User/Deployed, and CSM function is disabled.
Secure Boot Mode	Standard Custom	Secure Boot mode selector: In <b>Custom</b> mode, Secure Boot Variables can be configured without authentication

### Key Management

Allows you to provision advanced Secure Boot settings.

Aptio Setup Utility	– Copyr:	ight (C) 2017 Am Security	erican Megatrends, Inc.
Provision Factory Defaults		2]	Allow to provision factory default Secure Boot keys when System
Install Factory Default	keys		is in Setup Mode
▶ Enroll Efi Image			
Save all Secure Boot va	riables		
Secure Boot variable	Size  Ke	eys#  Key Source	
Platform Key(PK)		0 No Key	
▶ Key Exchange Keys		0  No Key	
Authorized Signatures	0	0  No Key	++: Select Screen
▶ Forbidden Signatures	0	0  No Key	↑↓: Select Item
Authorized TimeStamps	0	0  No Key	Enter: Select
OsRecovery Signatures	0	0  No Key	+/-: Change Opt.
			F1: General Help
			F2: Previous Values
			F3: Optimized Defaults
			F4: Save & Exit
			ESC: Exit

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ltem	Option	Description
Provision Factory	Disabled	Allows User to provision factory default Secure
Defaults	Enabled	Boot keys when System is in Setup Mode.
Install Factory Default	None	Forces System to User Mode - install all Factory
keys	None	Default keys
		Allows the image to run in Secure Boot mode.
Enroll Efi Image	None	Enroll SHA256 hash of the binary into Authorized
		Signature Database (db)

## **Boot Setup**

Use  $[\leftarrow] / [\rightarrow]$  to select [Boot] setup screen. Under this screen, you may use  $[\uparrow] [\downarrow]$  to select an item you would like to configure.

		– Copyright (C) 2018 Ameria m Socket Server Mgmt Sea	
	Boot Configuration Setup Prompt Timeout Bootup NumLock State Quiet Boot Boot mode select	5 [On] [Disabled] [LEGACY]	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
		ies [Hard Disk] [USB Device:LEI Virtual	
	Boot Option #3	CDROMO 1.00] [CD/DVD] [Network]	<pre>++: Select Screen 1↓: Select Item Enter: Select +/-: Change Opt.</pre>
•	USB Drive BBS Priorities	5	F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit

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ltem	Option	Description
Setup Prompt Timeout	5	Number of seconds to wait for setup activation key. 65535 means indefinite waiting.
Bootup NumLock State	<mark>On</mark> Off	Select the keyboard NumLock state
Quiet Boot	Disabled Enabled	Enables or disables Quiet Boot option.
Boot mode select	LEGACY UEFI DUAL	Select boot mode for LEGACY or UEFI.

- Choose boot priority from boot option group.
- Choose specific boot device priority sequence from available Group device.

## **Save and Exit Setup**

Use  $[\leftarrow] / [\rightarrow]$  to select [Save & Exit] setup screen. Under this screen, you may use [1] [1] to select an item you would like to configure.

Aptio Setup Utility – Copyright (C) 2018 ◀ Save & Exit	American Megatrends, Inc.	
Save Options Discard Changes and Exit Save Changes and Reset	Exit system setup without saving any changes.	
Default Options Restore Defaults		
Boot Override LEI Virtual CDROMO 1.00 LEI Virtual FloppyO 1.00		
LEI Virtual HDiskO 1.00 LEI Virtual CDROM1 1.00 LEI Virtual CDROM2 1.00	↔: Select Screen ↑↓: Select Item Enter: Select	
SRT USB 1100 Launch EFI Shell from filesystem device	+/-: Change Opt. F1: General Help	
	F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit	
Version 2.19.1268. Copyright (C) 2018 American Megatrends, Inc. AB		

#### ■Save Changes and Reset

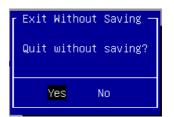
When Users have completed the system configuration changes, select this option to save the changes and Reset from BIOS Setup, so the new system configuration parameters can take effect. The following window will appear after the "Save Changes and Reset" option is selected. Select "**Yes**" to Save Changes and Reset Setup.



#### Discard Changes and Exit

Select this option to quit Setup without saving any modifications to the system configuration. The following window will appear after the "Discard Changes and Exit" option is selected. Select "**Yes**" to discard changes and Exit Setup.

Chapter 4: BIOS Setup



#### ■Restore Defaults

Restore default values for all setup options. Select "Yes" to load Optimized defaults.

Load	Optimized	Defaults —
Load	Optimized	Defaults?
	Yes	No

# **APPENDIX A: LED INDICATOR EXPLANATIONS**

The status explanations of LED indicators on Front Panel are as follows:



#### System Power

Solid Green	The system is powered on
Off	The system is powered off

#### System Status

This LED indicator is programmable. You could program it to display the operating status of the

behaviors described below:

Solid Green	Defined by GPIO
Solid Red	Defined by GPIO
Off	Defined by GPIO

#### HDD Activity

If this LED blinks, it indicates data access activities; otherwise, it remains off.

Blinking Amber	Data access activity
Off	No data access activity

Speed



Link Activity

#### Link Activity

Blinking Amber   Link has been established and there is activity on this port	
Solid Amber	Link has been established and there is no activity on this port
Off	No link has been established

#### Speed

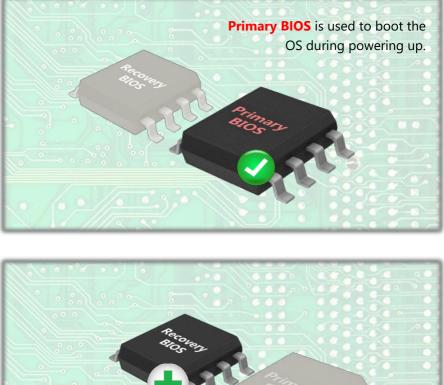
Solid Amber Operating as a Gigabit connection (1000 Mbps)	
Solid Green	Operating as a 100-Mbps connection
Off	Operating as a 10-Mbps connection

# **APPENDIX B: DUAL BIOS INTRODUCTION**

# Why Dual BIOS?

Failure of booting up BIOS is not uncommon to most experienced users, yet it can be the worst nightmare. This occurs mostly during a power failure or a mishandled BIOS update, after a malware's attack that corrupted the data on it, or, at worst, due to physical damage that caused the BIOS not to function. However, when it happens, not merely will the recovering procedures consume considerable time and effort, but all you work might also be to no avail. Eventually, you are left with no choice but to ship the board back to the manufacturer.

Lanner understands this pain and has empowered our products with the Dual BIOS feature. Normally, the Primary BIOS is used to boot the OS during powering up; when Primary BIOS goes down, the Recovery BIOS automatically kicks in to boot up the OS for you to take further steps such as performing data backup and BIOS upgrade.





## **How Dual BIOS Works**

Dual BIOS features two physical BIOS ROMs soldered onto the motherboard, carrying two separate BIOS images:

Primary BIOS	Recovery BIOS
Default: Factory	Default: Factory
Yes	No
(Read, Write)	(Read only)
	Default: Factory Yes

Primary BIOS vs. Recovery BIOS

The Primary BIOS carries the image used for system boot-up, the parameters of which can be overwritten by custom settings; while the Recovery BIOS carries the read-only image locked to the factory default, which guarantees a safe and successful system boot-up.

If the Primary BIOS fails or stops functioning, the system will automatically invoke a restart followed by a boot-up from the Recovery BIOS. The entire process is completely automatic.

# **APPENDIX C: SETTING UP CONSOLE REDIRECTIONS**

Console redirection lets you monitor and configure a system from a remote terminal computer by re-directing keyboard input and text output through the serial port. The following steps illustrate how to use this feature. The BIOS of the system allows the redirection of the console I/O to a serial port. With this configured, you can remotely access the entire boot sequence through a console port.

- **1.** Connect one end of the console cable to console port of the system and the other end to the serial port of the Remote Client System.
- 2. Configure the following settings in the BIOS Setup menu:

BIOS > Advanced > Serial Port Console Redirection > Console Redirection Settings, select 115200 for the Baud Rate, None. for Flow control, 8 for the Data Bit, None for Parity Check, and 1 for the Stop Bit.

**3.** Configure console redirection related settings on the client system. You can use a terminal emulation program that features communication with serial COM ports such as *TeraTerm* or *Putty*. Make sure the serial connection properties of the client conform to those set in Step 1 for server.

# APPENDIX D: PROGRAMMING GENERATION 3 LAN BYPASS

The bypass function is used to link two independent Ethernet ports when the system crashes or powers off. This means if your system is equipped with a LAN Bypass function, a condition in your system will not interrupt your network traffic. Different from the previous two generations (Gen1 and Gen2), the Lanner Bypass Gen 3 employs a programming method to control the bypass function by software. There are typically two types of communication status for the bypass function, one is "**Normal** " and another is "Bypass " status. Furthermore, the Lanner Bypass software is capable of controlling the bypass status in the following 3 instances.

- ▶ When the system powers off, it can be forced to enable the LAN Bypass function.
- ▶ When the system is in the just-on state which is a brief moment when it powers up.
- The Lanner bypass possesses the following features:
  - 1. Communication through SMBUS (I2C)
  - 2. Independent bypass status control for each pair up to a total of 4 pairs
  - **3.** Lanner Bypass Modules can bypass systems Ethernet ports on a host system during three instances: Just-on (Just-on is the brief moment when the internal power supply turns on and booting process starts), system off, or upon software request (during run-time).
  - **4.** Software programmable bypass or normal mode
  - 5. Software programmable timer interval:
    - **JUST-ON** watchdog timer, used during JUST-ON, has timer setting of 5~1275 seconds of timer interval.
    - Run-Time watchdog timer, used during run-time, with of 1~255 seconds of timer interval.
  - 6. Multiple Watchdog Timers:

-**Two for run-time**: It is designed to give you a more variety of controls of the bypass on port basis. By using dedicated watchdogs for different pairs of bypass, you have the flexibility to manage the bypass status for them differently.

-**One for just-on**: It is designed to give you the precise control of the bypass during this phase. You can use this timer to delay enabling the bypass in just-on state.

For a reference utility that contains sample code for LAN Bypass function programming, please visit <u>http://www.lannerinc.com/support/download-center/drivers</u>, enter the product category and download the utility package of this system.

# APPENDIX E: INSTALLING INTEL® LAN CONTROLLER DRIVER FOR LINUX

For the latest driver update, please visit Intel<sup>®</sup> download center at <u>https://downloadcenter.intel.com/</u>, use the keyword search or the filter to access the driver's product page, and then download the latest controller driver as well as the ReadMe document.

Product Name Keyword	1210
Download Type	Drivers
Operating System	Linux*
Product page	Downloads for Intel <sup>®</sup> Ethernet Controller I210 Series

# **APPENDIX F: TERMS AND CONDITIONS**

# **Warranty Policy**

- **1.** All products are under warranty against defects in materials and workmanship for a period of one year from the date of purchase.
- **2.** The buyer will bear the return freight charges for goods returned for repair within the warranty period; whereas the manufacturer will bear the after service freight charges for goods returned to the user.
- **3.** The buyer will pay for repair (for replaced components plus service time) and transportation charges (both ways) for items after the expiration of the warranty period.
- **4.** If the RMA Service Request Form does not meet the stated requirement as listed on "RMA Service", RMA goods will be returned at customer's expense.
- 5. The following conditions are excluded from this warranty:
  - Improper or inadequate maintenance by the customer
  - ▶ Unauthorized modification, misuse, or reversed engineering of the product
  - Operation outside of the environmental specifications for the product.

## **RMA Service**

#### **Requesting an RMA#**

- 1. To obtain an RMA number, simply fill out and fax the "RMA Request Form" to your supplier.
- **2.** The customer is required to fill out the problem code as listed. If your problem is not among the codes listed, please write the symptom description in the remarks box.
- **3.** Ship the defective unit(s) on freight prepaid terms. Use the original packing materials when possible.
- **4.** Mark the RMA# clearly on the box.



**Note**: Customer is responsible for shipping damage(s) resulting from inadequate/loose packing of the defective unit(s). All RMA# are valid for 30 days only; RMA goods received after the effective RMA# period will be rejected.

## **RMA Service Request Form**

When requesting RMA service, please fill out the following form. Without this form enclosed, your RMA cannot be processed.

RMA No:			Reasons to Return:   Repair(Please include failure details)  Testing Purpose	
Compa	any:	Contact Person:		
Phone	No.	Purchased Date	:	
Fax No.:		Applied Date:		
Return	n Shipping Addr	ess:		
	ng by: □ Air Fre ers:	eight □ Sea □ Express_ 		
Item	Model Name	Serial Number	Configuration	

Item	Problem Code	Failure Status

\*Problem Code: 01:D.O.A. R.M.A. 04: FDC Fail 05: HDC Fail 06: Bad Slot

07: BIOS Problem 02: Second Time 08: Keyboard Controller Fail 09: Cache RMA Problem 03: CMOS Data Lost 10: Memory Socket Bad 11: Hang Up Software 12: Out Look Damage

13: SCSI 19: DIO 14: LPT Port 20: Buzzer 21: Shut Down 15: PS2 16: LAN 22: Panel Fail 17: COM Port 23: CRT Fail 18: Watchdog Timer 24: Others (Pls specify)

Request	Party
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**Confirmed By Supplier** 

Authorized Signature / Date

Authorized Signature / Date

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